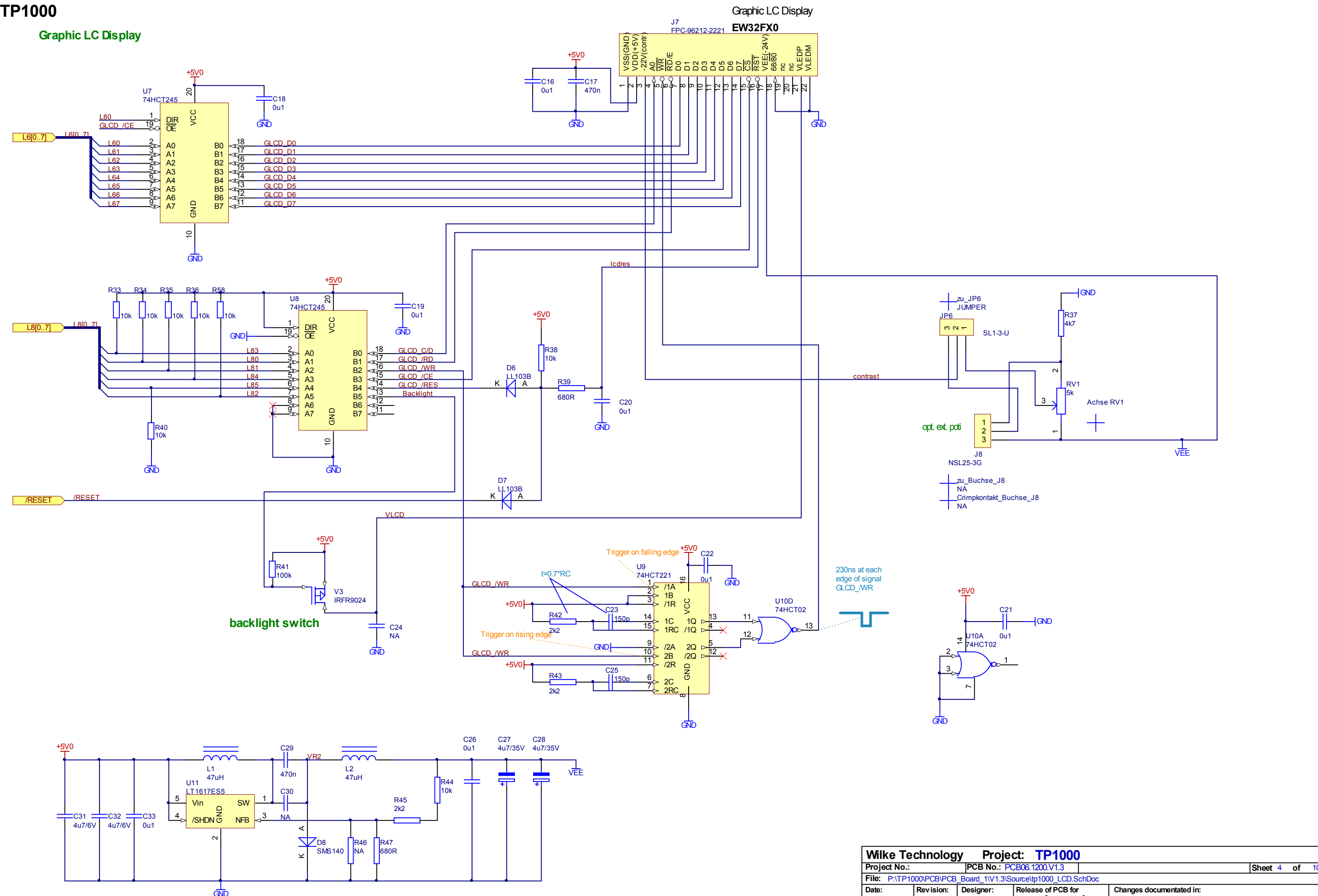


Wilke Technology Project: TP1000				
Project No.:		PCB No.: PCB06.1200.V1.3		Sheet 3 of 10
File: P:\TP1000\PCB\PCB_Board_1\1V1.3\Source\tp1000_ethernet.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype ¹ pilot series ² series	Changes documented in:
14. Mai 2006	V1.0	K. Hiltrop	• •	P:\TP1000\PCB\History_TP1000.doc
16. Aug 2006	V1.1	W. Silix	• •	P:\TP1000\PCB\History_TP1000.doc
21. Nov 2006	V1.2	W. Silix	• •	P:\TP1000\PCB\History_TP1000.doc
29. Dez 2006	V1.3	W. Silix	• •	P:\TP1000\PCB\History_TP1000.doc

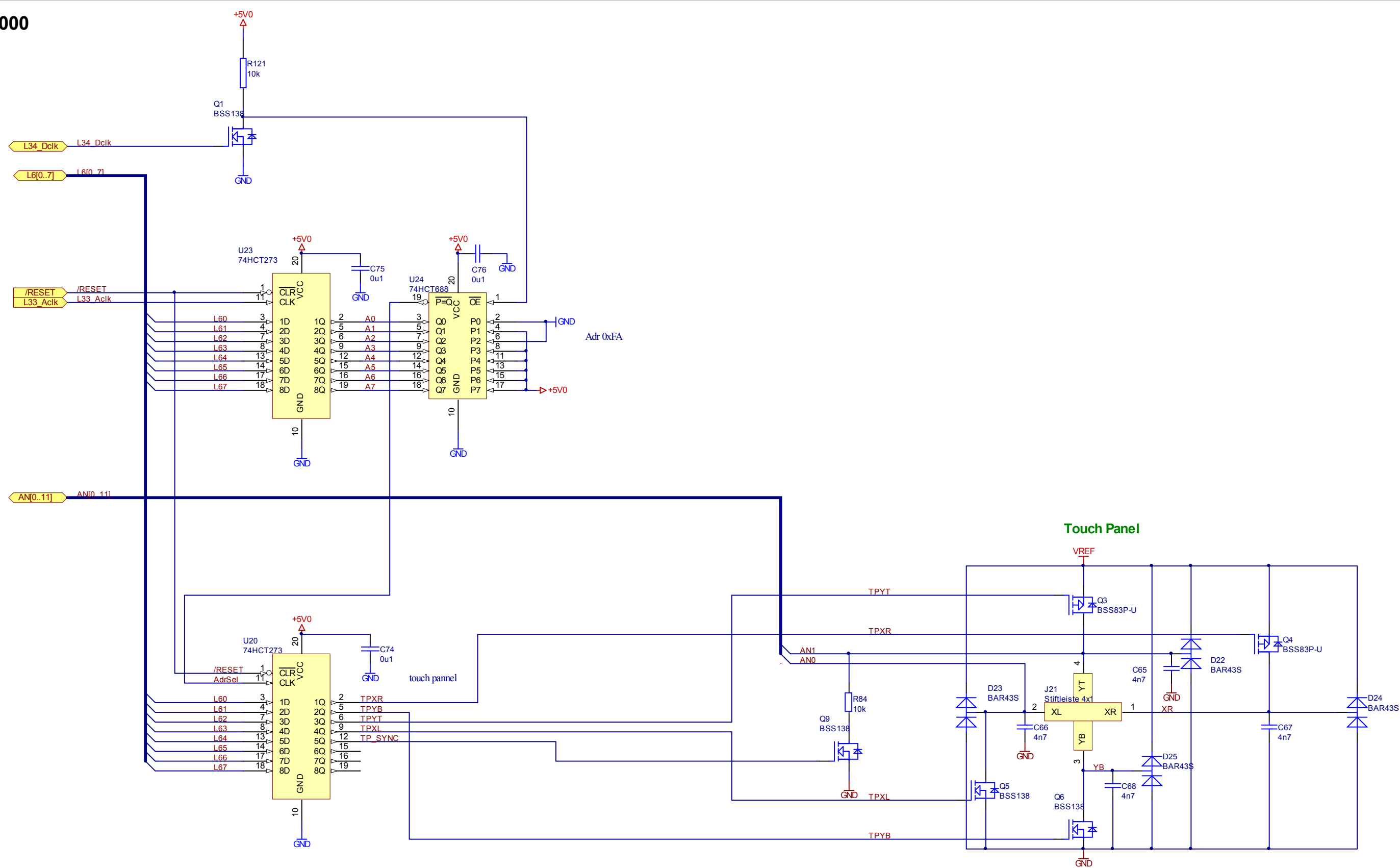
TP1000

Graphic LC Display

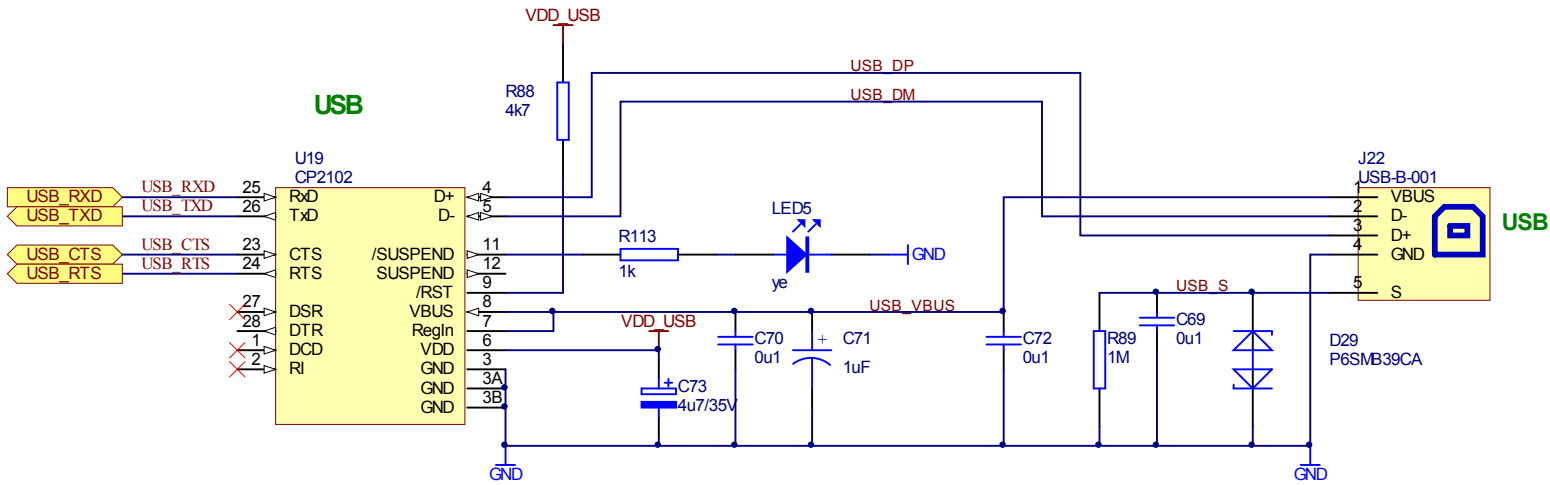


Wilke Technology		Project: TP1000	
Project No.:		PCB No.: PCB06.1200.V1.3	
File: P:\TP1000\PCB\PCB_Board_1\V1.3\Source\tp1000_LCD.SchDoc		Sheet 4 of 10	
Date:	Revision:	Designer:	Release of PCB for:
			Prototype pilot series
14. Mai 2006	V1.0	K. Hiltrop	• •
16. Aug 2006	V1.1	W. Silex	• •
21. Nov 2006	V1.2	W. Silex	• •
29. Dez 2006	V1.3	W. Silex	• •
			• •
			Changes documented in:
			P:\TP1000\PCB\History_TP1000.doc
			P:\TP1000\PCB\History_TP1000.doc
			P:\TP1000\PCB\History_TP1000.doc

TP1000

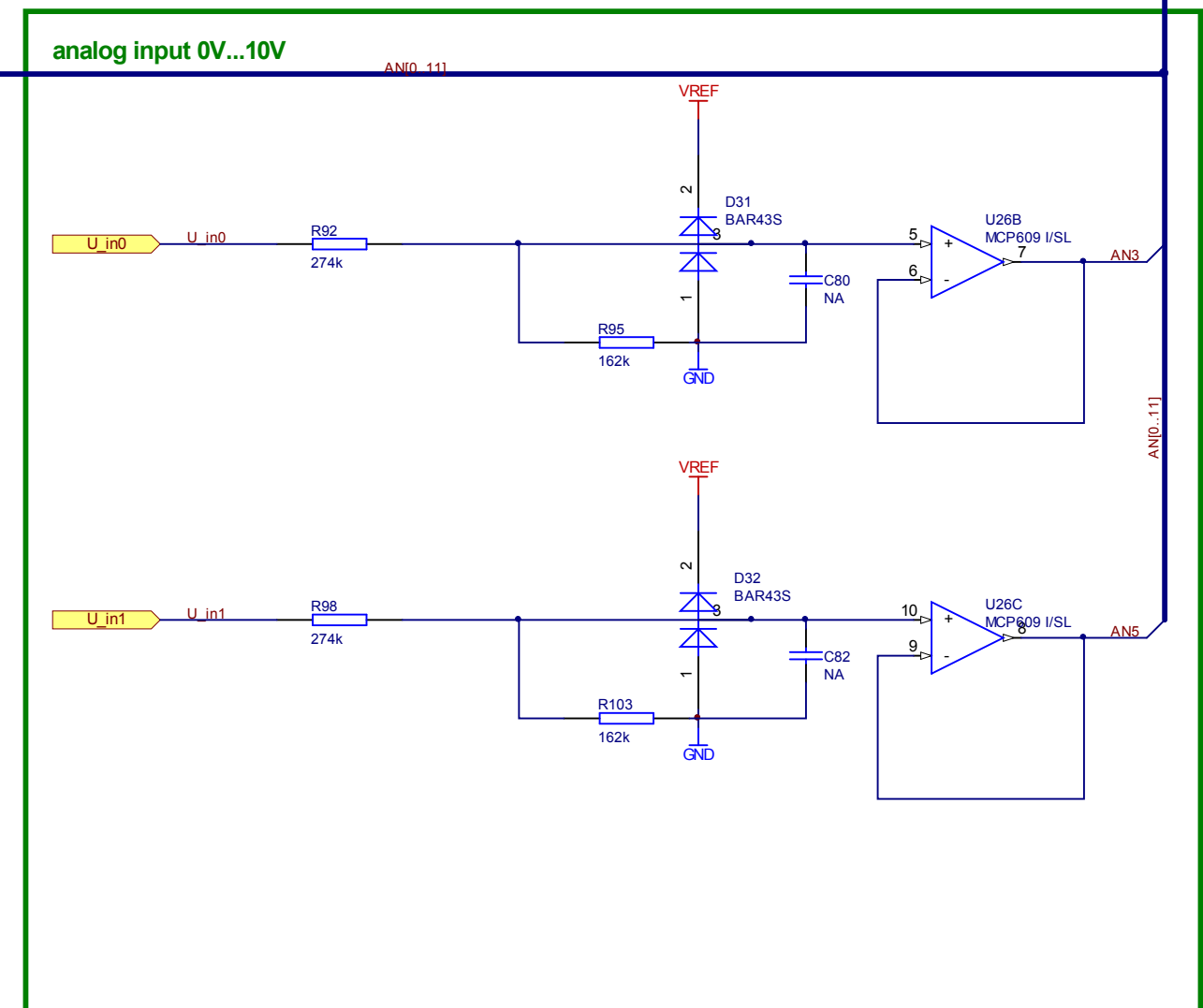
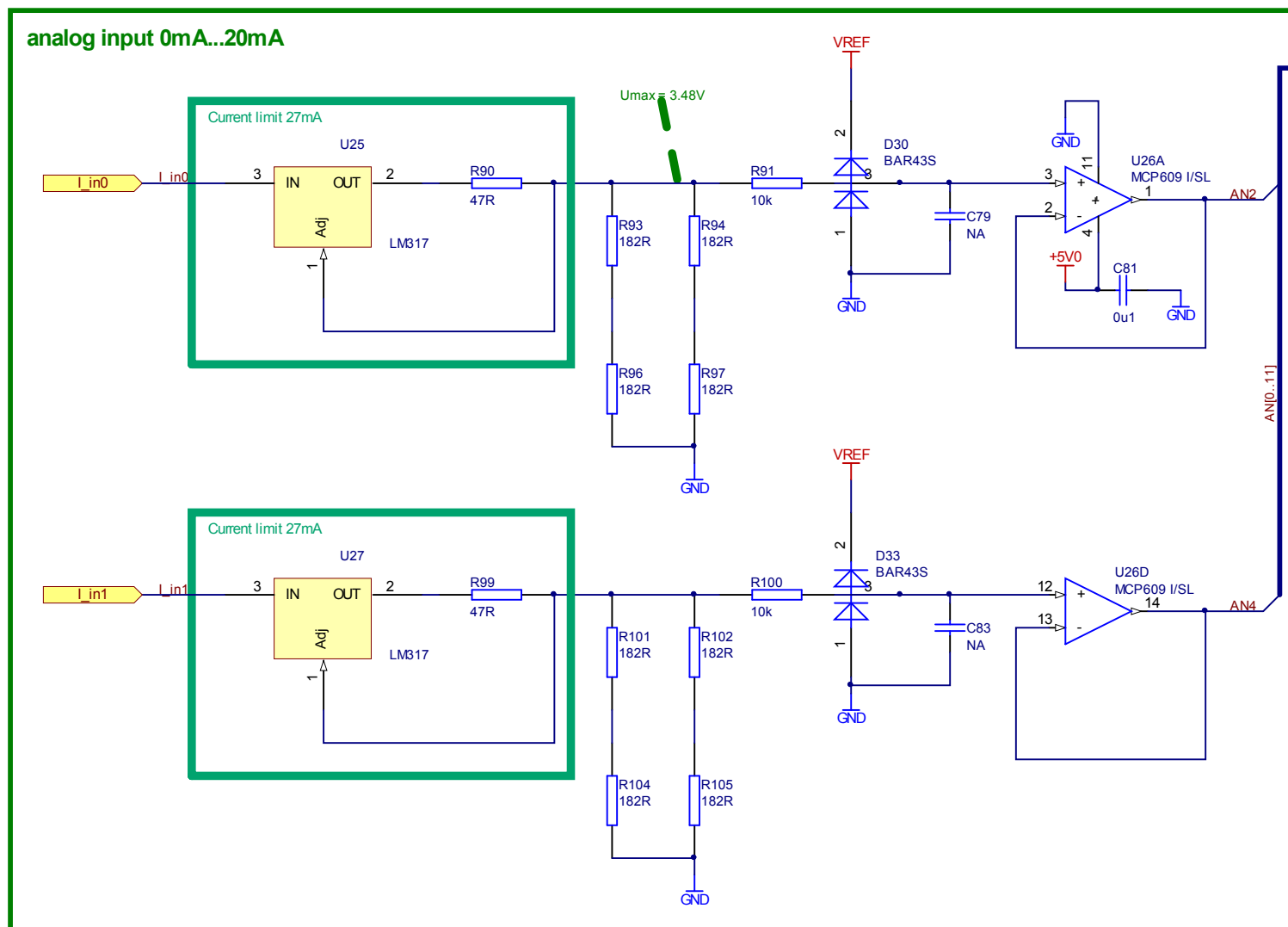


Wilke Technology					Project: TP1000				
Project No.:		PCB No.: PCB06.1200.V1.3						Sheet 5 of 10	
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Date:	Revision:	Designer:	Release of PCB for Prototype pilot series series		Changes documented in:				
14. Mai 2006	V1.0	K. Hiltrop	• •		P:\TP1000\PCB\History_TP1000.doc				
16. Aug 2006	V1.1	W. Silex	• •		P:\TP1000\PCB\History_TP1000.doc				
21. Nov 2006	V1.2	W. Silex	• •		P:\TP1000\PCB\History_TP1000.doc				
29. Dez 2006	V1.3	W. Silex	• •						

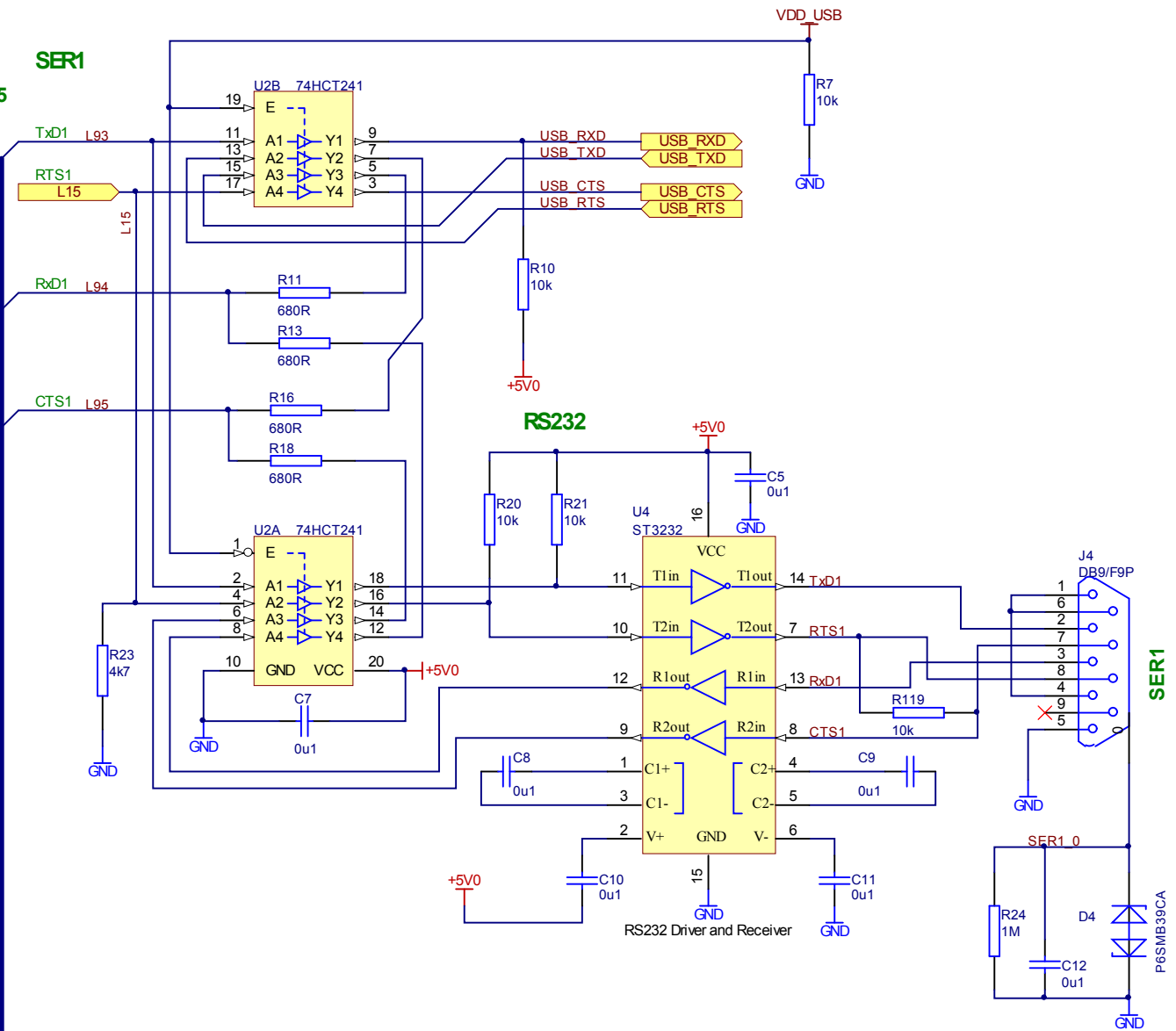
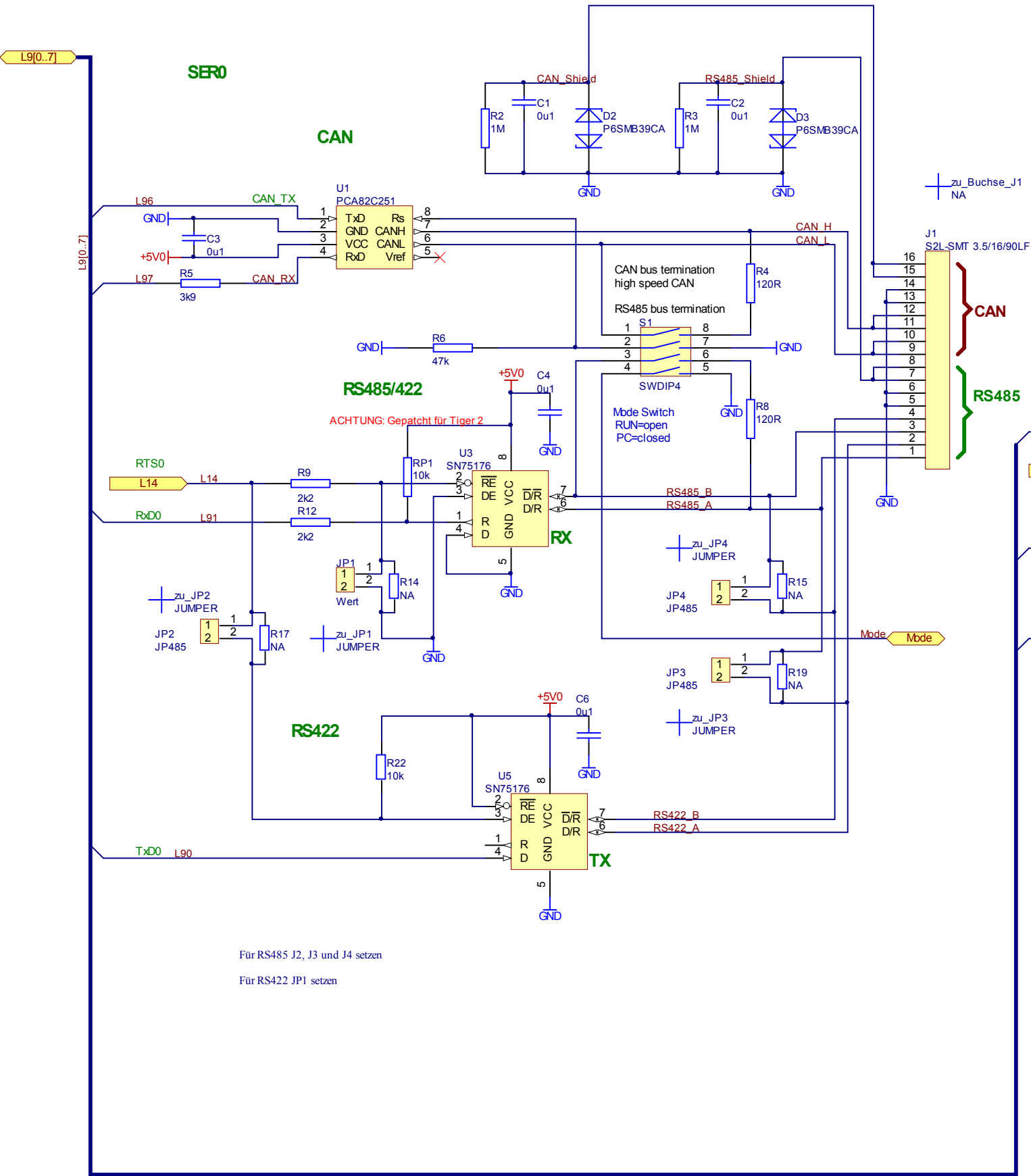


Wilke Technology Project: TP1000				
Project No.:		PCB No.: PCB06.1200.V1.3		Sheet 6 of 10
File: P:\TP1000\PCB\PCB_Board_1\1V1.3\Source\tp1000_usb.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype ¹ pilot series ² series	Changes documented in:
14. Mai 2006	V1.0	K. Hiltrop	•	P:\TP1000\PCB\History_TP1000.doc P:\TP1000\PCB\History_TP1000.doc P:\TP1000\PCB\History_TP1000.doc
16. Aug 2006	V1.1	W. Silex	•	
21. Nov 2006	V1.2	W. Silex	•	
29. Dez 2006	V1.3	W. Silex	•	
			•	

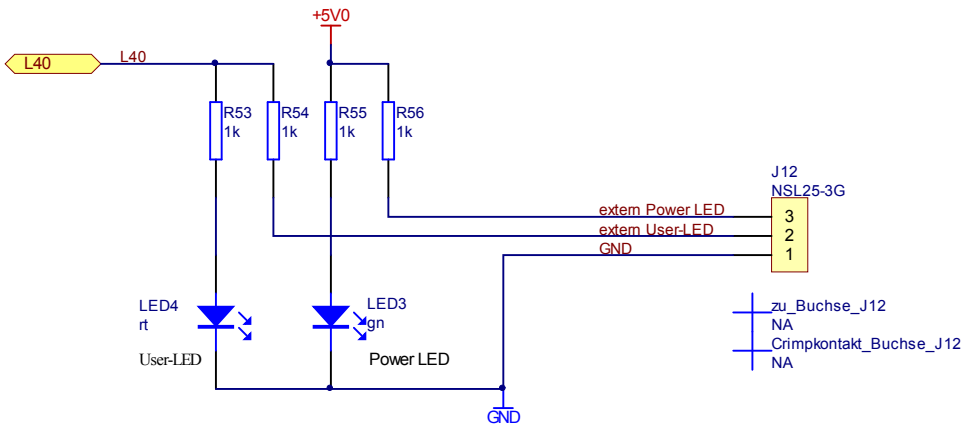
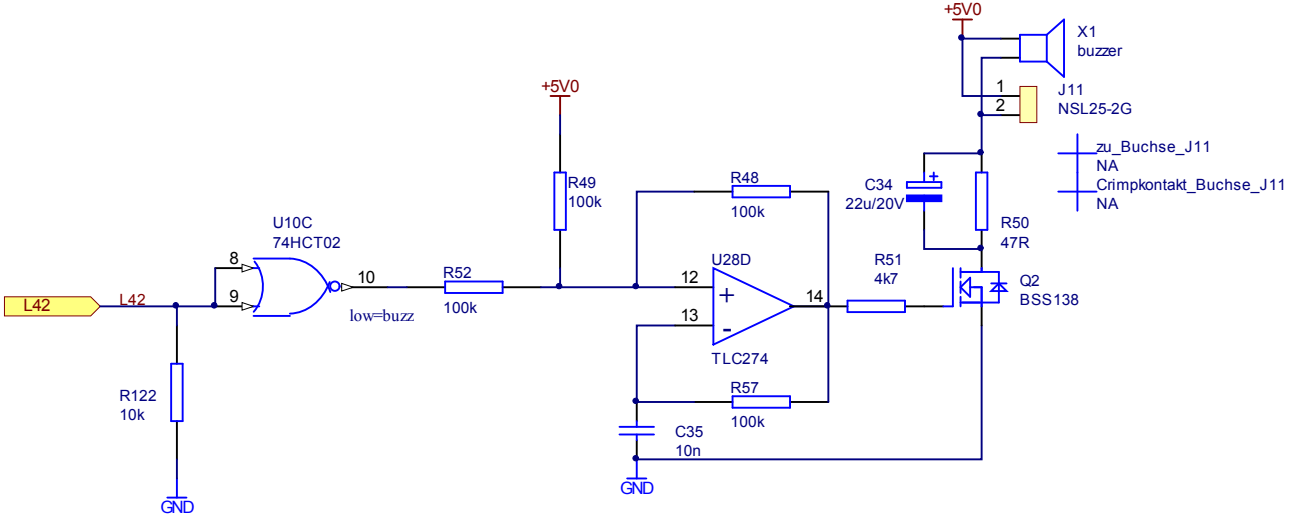
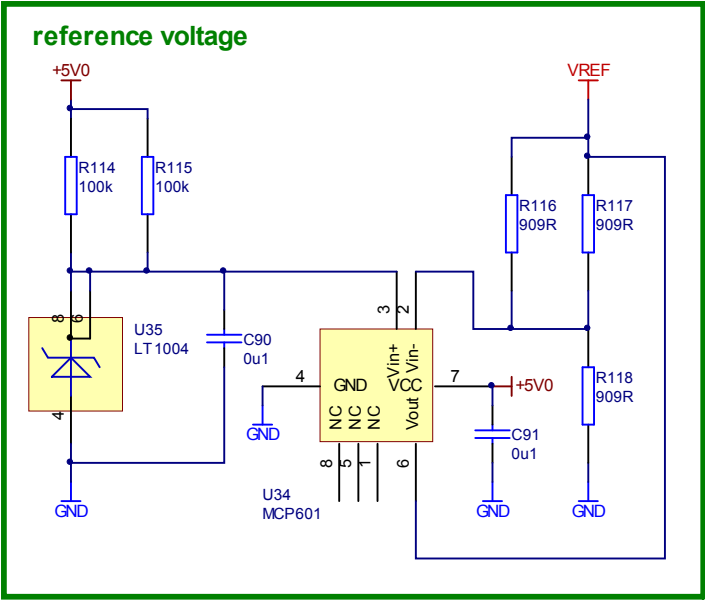
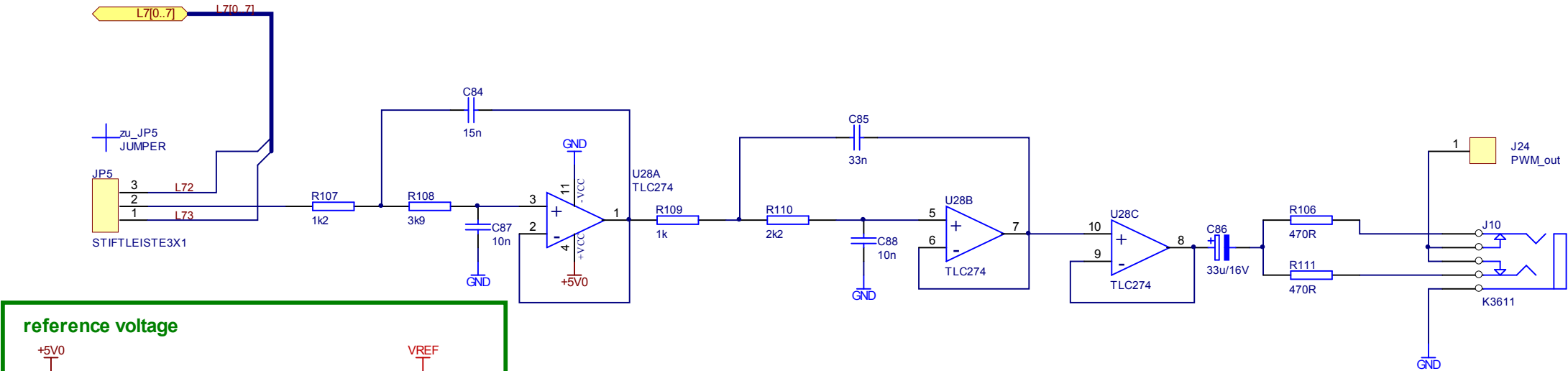
TP1000 Analog Inputs



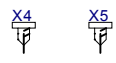
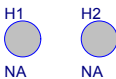
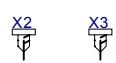
Wilke Technology					Project: TP1000				
Project No.:		PCB No.: PCB06.1200.V1.3			Sheet 7 of 10				
File: P:\TP1000\PCB\PCB_Board_1W1.3\Source\tp1000_analog.SchDoc									
Date:	Revision:	Designer:	Release of PCB for		Changes documented in:				
			Prototype		pilot series				
14. Mai 2006	V1.0	K. Hiltrop	.	.					
16. Aug 2006	V1.1	W. Silex	.	.	P:\TP1000\PCB\History_TP1000.doc				
21. Nov 2006	V1.2	W. Silex	.	.	P:\TP1000\PCB\History_TP1000.doc				
29. Dez 2006	V1.3	W. Silex	.	.	P:\TP1000\PCB\History_TP1000.doc				
			.	.					



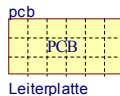
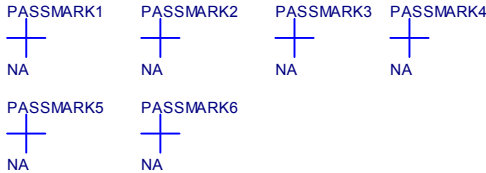
Wilke Technology		Project: TP1000	
Project No.:	PCB No.: PCB06.1200.V1.3	Sheet 8 of 10	
File: P:\TP1000\PCB\PCB_Board_1\V1.3\Source\tp1000_comm.SchDoc			
Date:	Revision:	Designer:	Release of PCB for Prototype pilot series series
14. Mai 2006	V1.0	K. Hiltrop	• •
16. Aug 2006	V1.1	W. Silex	• •
21. Nov 2006	V1.2	W. Silex	• •
29. Dez 2006	V1.3	W. Silex	• •
			Changes documented in:
			P:\TP1000\PCB\History_TP1000.doc
			P:\TP1000\PCB\History_TP1000.doc
			P:\TP1000\PCB\History_TP1000.doc



Screws...M4



Gehäuseverbindung nach GND



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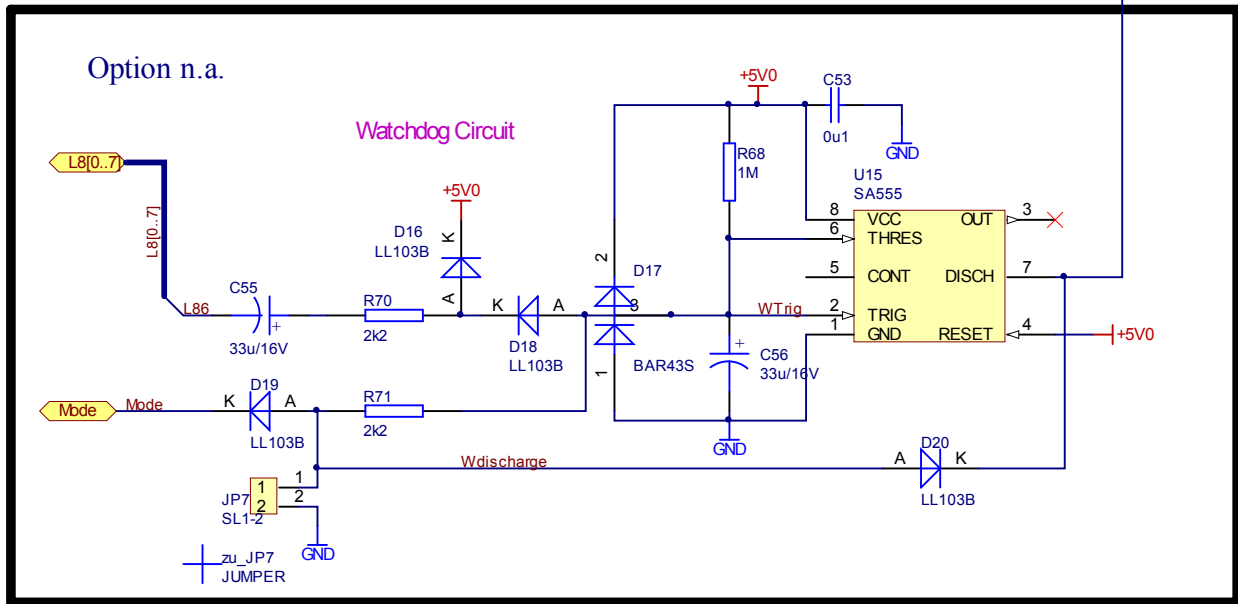
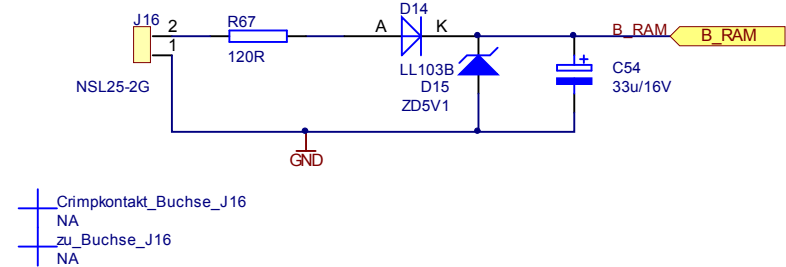
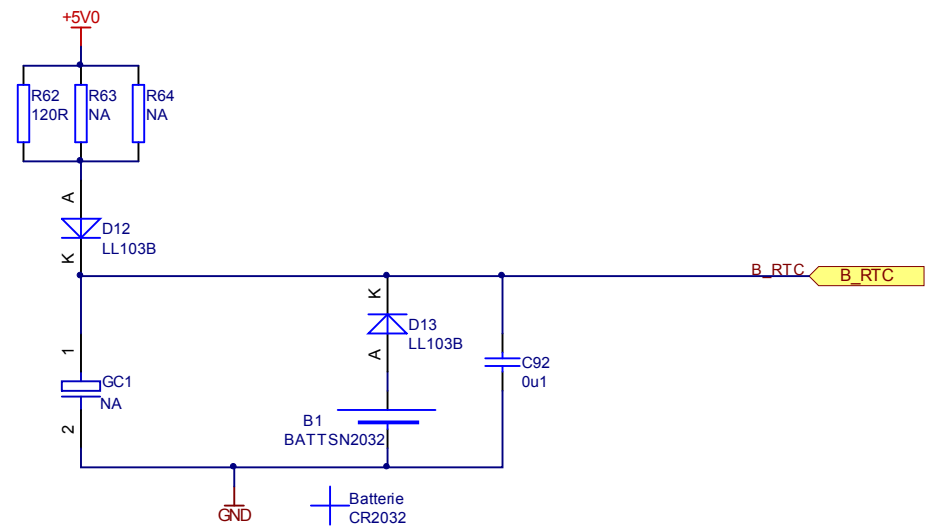
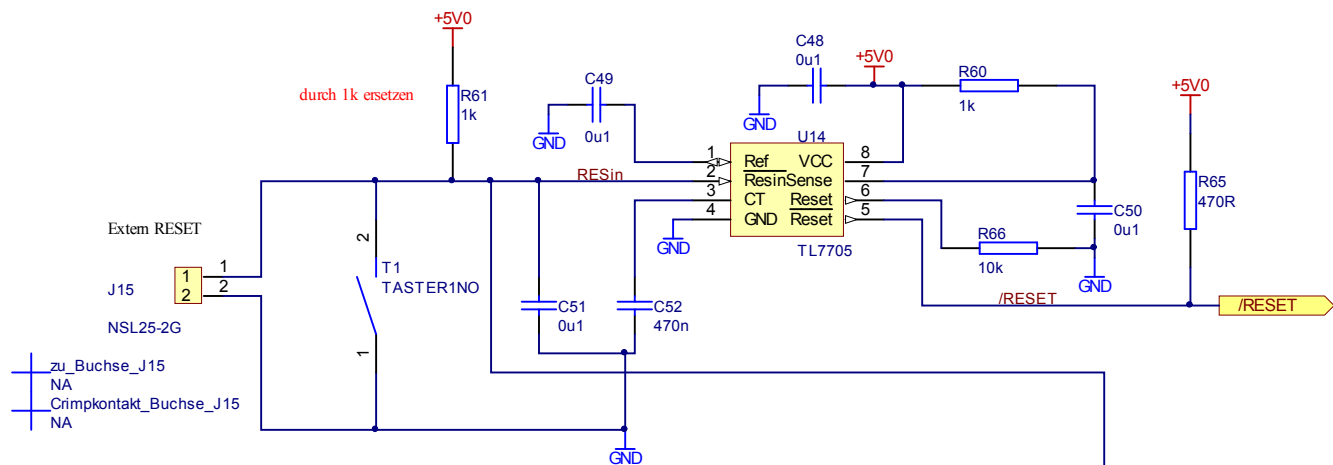
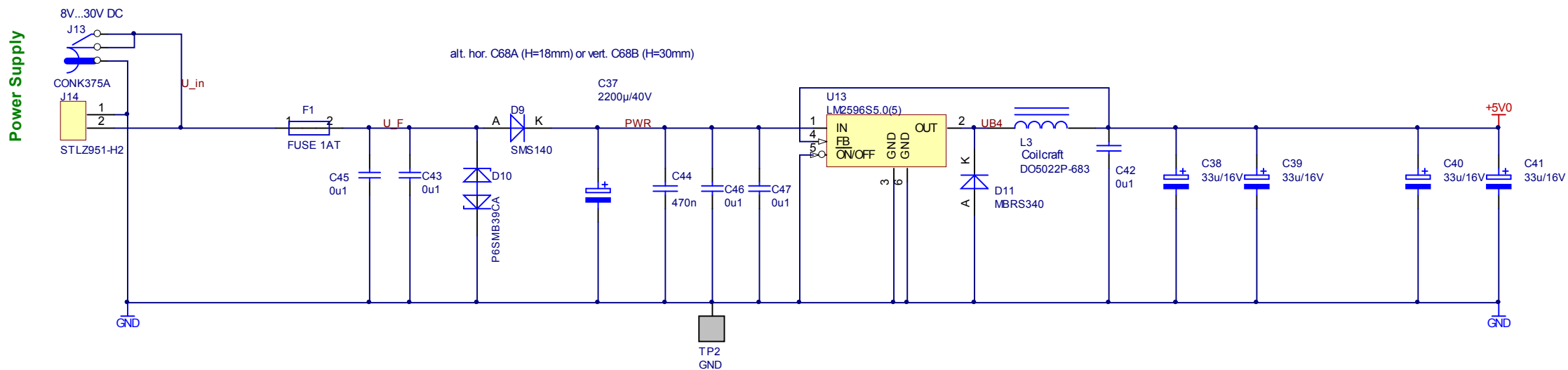
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Date:	Revision:	Designer:	Release of PCB for	Changes documented in:
14. Mai 2006	V1.0	K. Hiltrop	Prototype	
16. Aug 2006	V1.1	W. Silix	pilot series	
21. Nov 2006	V1.2	W. Silix	series	
29. Dez 2006	V1.3	W. Silix		

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TP1000

power supply



Wilke Technology		Project: TP1000	
Project No.:	PCB No.: PCB06.1200.V1.3	Sheet 10 of 10	
File: P:\TP1000\PCB\PCB_Board_1\V1.3\Source\tp1000_power.SchDoc			
Date:	Revision:	Designer:	Release of PCB for Prototype pilot series
14. Mai 2006	V1.0	K. Hiltrop	• •
16. Aug 2006	V1.1	W. Silex	• •
21. Nov 2006	V1.2	W. Silex	• •
29. Dez 2006	V1.3	W. Silex	• •
			Changes documented in:
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