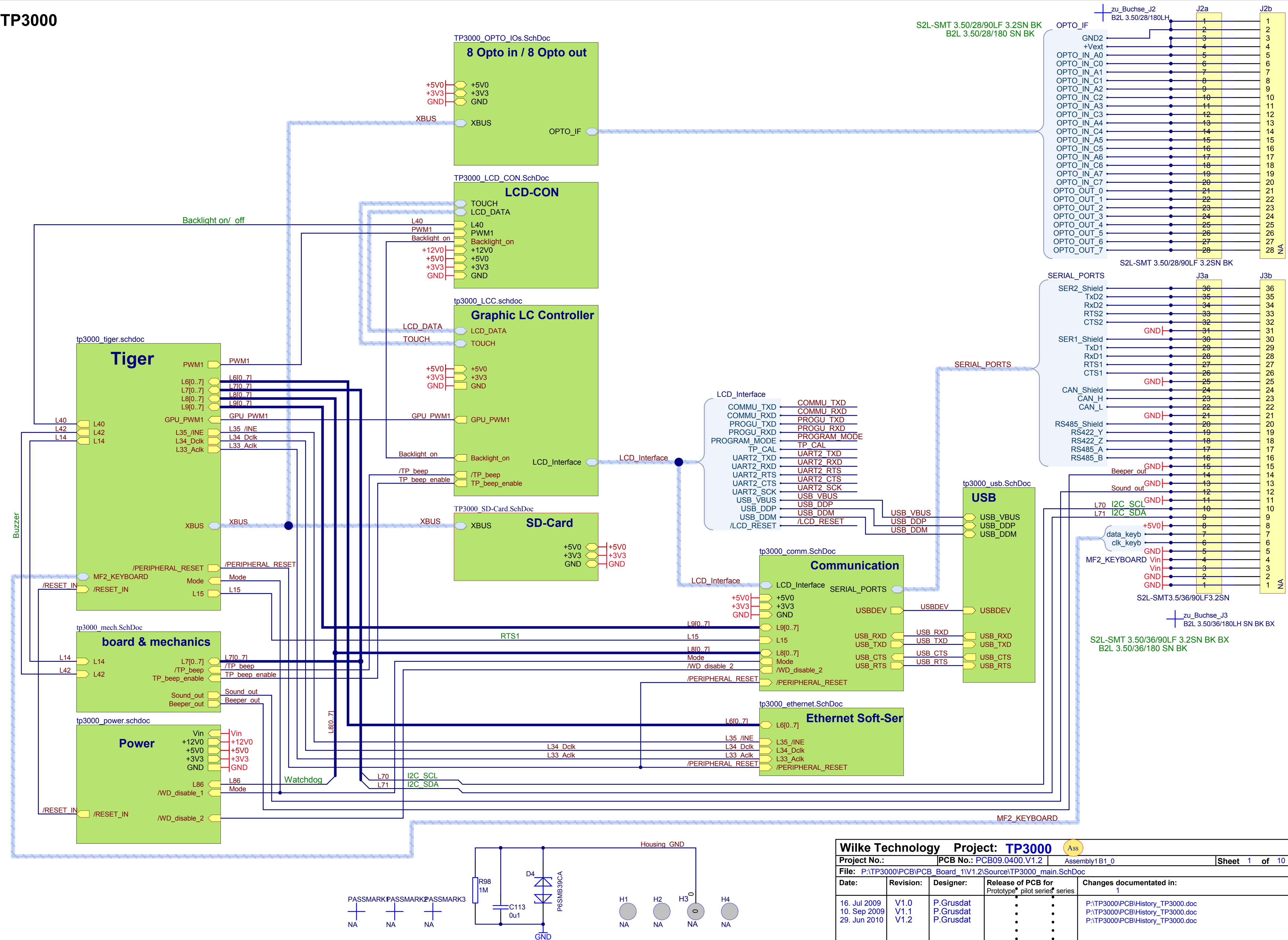
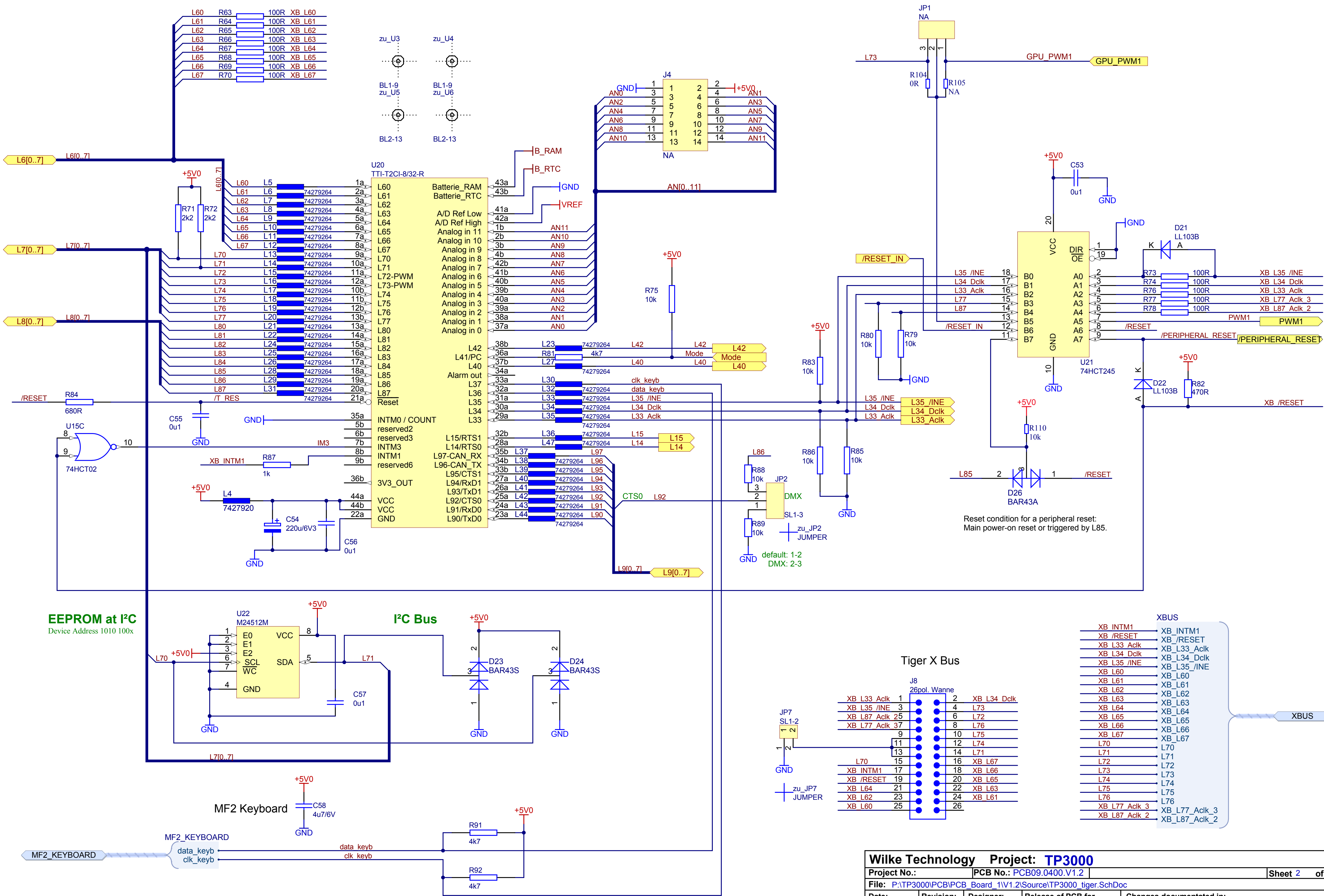


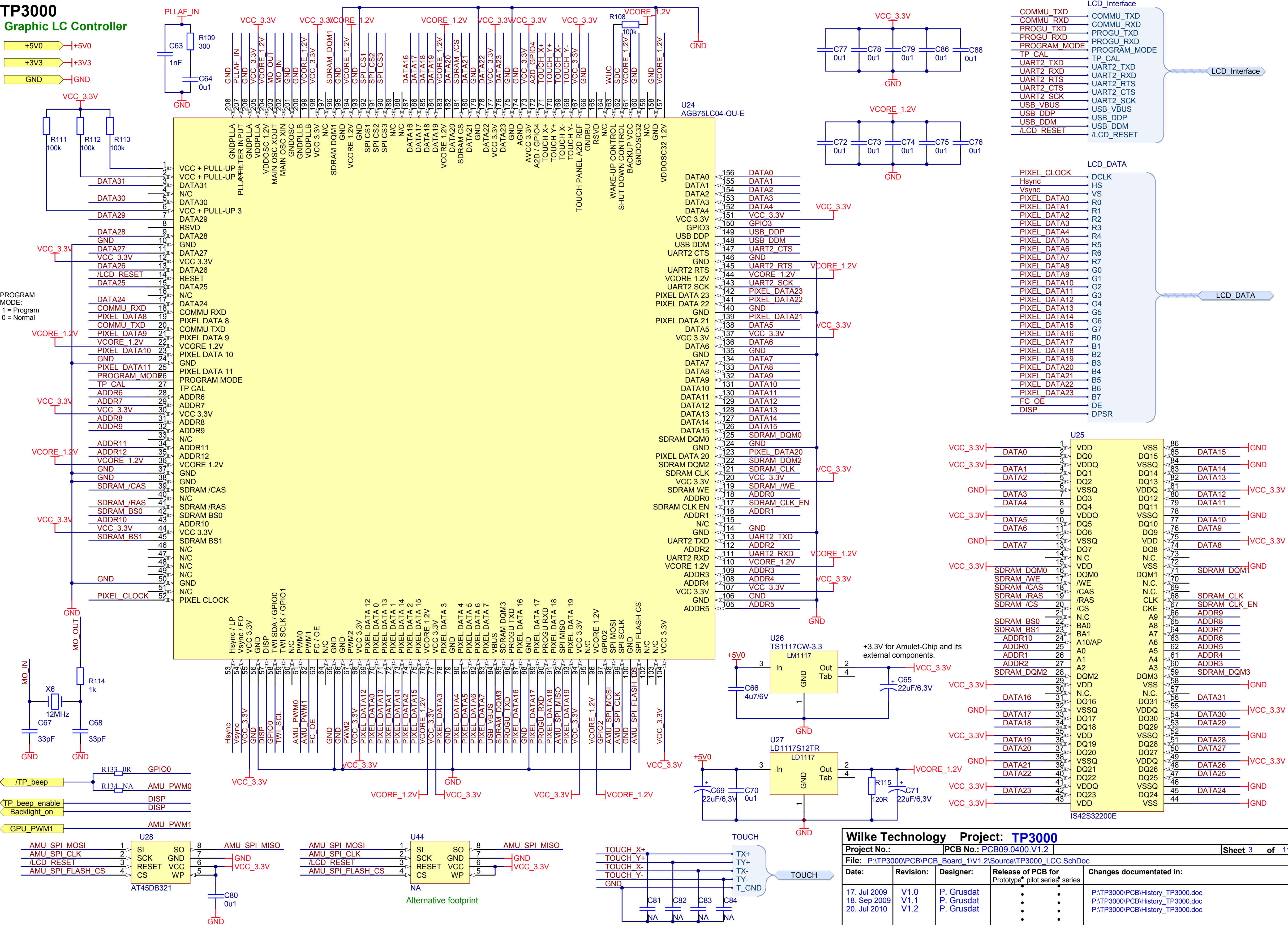
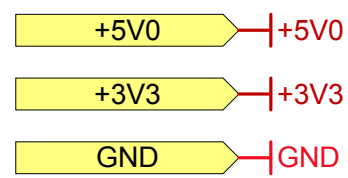


Wilke Technology Project: TP3000				
Project No.:		PCB No.: PCB09.0400.V1.2		Assembly B1_0
File: P:\TP3000\PCB\PCB_Board_1V1.2\Source\TP3000_main.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype* pilot series*	Changes documented in: 1
16. Jul 2009	V1.0	P.Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
10. Sep 2009	V1.1	P.Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
29. Jun 2010	V1.2	P.Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
			• •	



Wilke Technology Project: TP3000				
Project No.:		PCB No.: PCB09.0400.V1.2		Sheet 2 of 11
File: P:\TP3000\PCB\PCB_Board_1\1.2\Source\TP3000_tiger.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype* pilot series* series	Changes documented in:
27. Juli 2009	V1.0	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
18. Sep 2009	V1.1	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
20. Jul. 2010	V1.2	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
			• •	
			• •	

TP3000
Graphic LC Controller



LCD_Interface

COMMU_TXD	COMMU_TXD
COMMU_RXD	COMMU_RXD
PROGU_TXD	PROGU_TXD
PROGU_RXD	PROGU_RXD
PROGRAM_MODE	PROGRAM_MODE
TP_CAL	TP_CAL
UART2_TXD	UART2_TXD
UART2_RXD	UART2_RXD
UART2_RTS	UART2_RTS
UART2_CTS	UART2_CTS
UART2_SCK	UART2_SCK
USB_VBUS	USB_VBUS
USB_DDP	USB_DDP
USB_DDM	USB_DDM
/LCD_RESET	/LCD_RESET

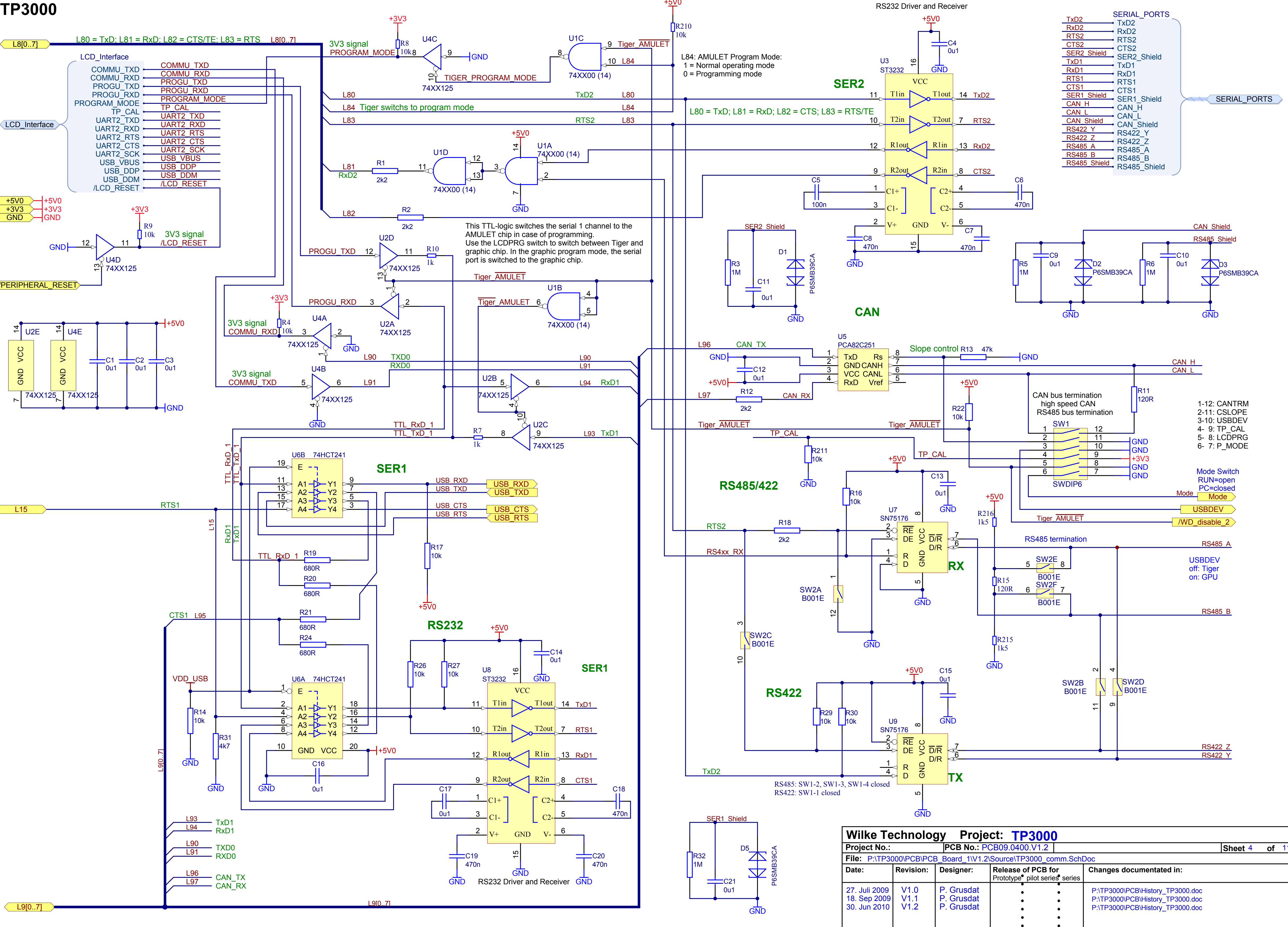
LCD_DATA

PIXEL_CLOCK	DCLK
Hsync	HS
Vsync	VS
PIXEL_DATA0	R0
PIXEL_DATA1	R1
PIXEL_DATA2	R2
PIXEL_DATA3	R3
PIXEL_DATA4	R4
PIXEL_DATA5	R5
PIXEL_DATA6	R6
PIXEL_DATA7	R7
PIXEL_DATA8	G0
PIXEL_DATA9	G1
PIXEL_DATA10	G2
PIXEL_DATA11	G3
PIXEL_DATA12	G4
PIXEL_DATA13	G5
PIXEL_DATA14	G6
PIXEL_DATA15	G7
PIXEL_DATA16	B0
PIXEL_DATA17	B1
PIXEL_DATA18	B2
PIXEL_DATA19	B3
PIXEL_DATA20	B4
PIXEL_DATA21	B5
PIXEL_DATA22	B6
PIXEL_DATA23	B7
FC_OE	DE
DISP	DPSR

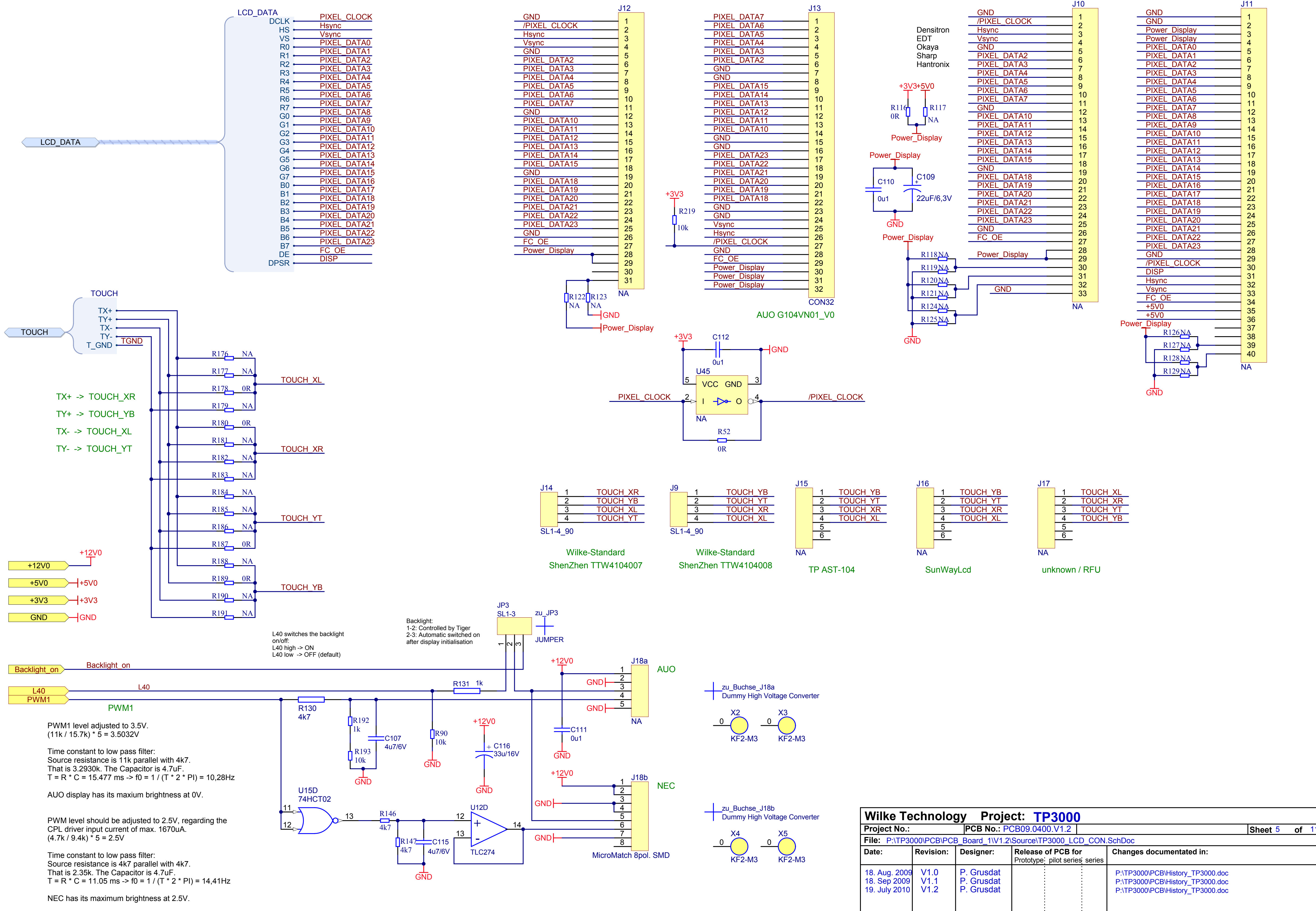
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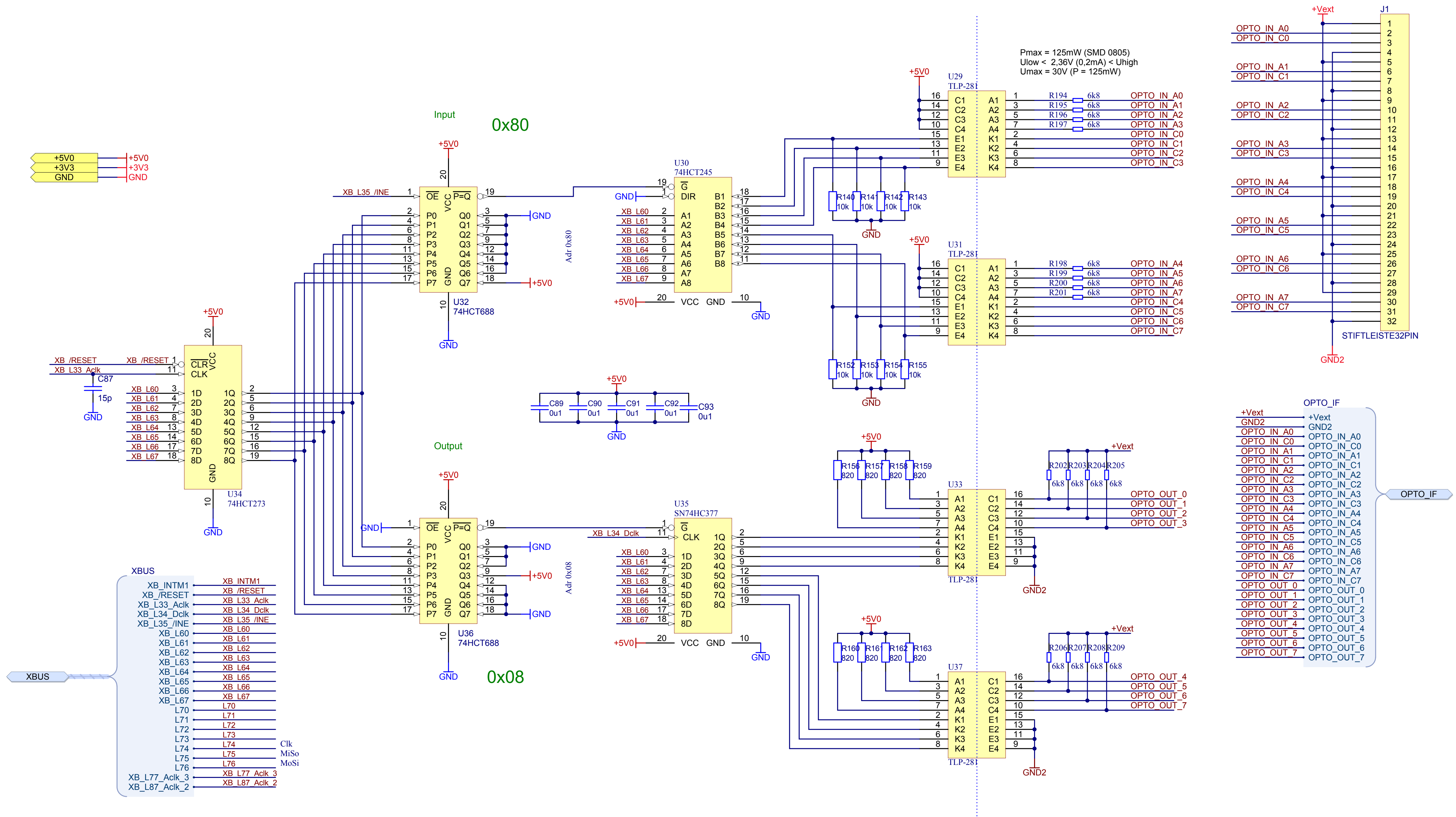
VCC_3.3V	DATA0	1	VDD	86	VSS	86	DATA15	GND
VCC_3.3V	DATA1	2	DQ0	85	DQ15	85	DATA14	GND
VCC_3.3V	DATA2	3	VDDQ	84	VSSQ	83	DATA13	GND
GND	DATA3	4	DQ1	83	DQ14	82	DATA12	VCC_3.3V
GND	DATA4	5	DQ2	82	DQ13	81	DATA11	VCC_3.3V
VCC_3.3V	DATA5	6	VSSQ	81	VDDQ	80	DATA10	GND
VCC_3.3V	DATA6	7	DQ3	80	DQ12	79	DATA9	VCC_3.3V
GND	DATA7	8	DQ4	79	DQ11	78	DATA8	VCC_3.3V
VCC_3.3V	DATA8	9	VDDQ	78	VSSQ	77	DATA7	GND
GND	DATA9	10	DQ5	77	DQ10	76	DATA6	VCC_3.3V
VCC_3.3V	DATA10	11	DQ6	76	DQ9	75	DATA5	VCC_3.3V
GND	DATA11	12	VSSQ	75	VDD	74	DATA4	VCC_3.3V
VCC_3.3V	DATA12	13	DQ7	74	DQ8	73	DATA3	GND
VCC_3.3V	DATA13	14	N.C.	73	N.C.	72	DATA2	GND
VCC_3.3V	DATA14	15	VDD	72	VSS	71	DATA1	GND
VCC_3.3V	DATA15	16	DQM0	71	DQM1	70	DATA0	GND
VCC_3.3V	DATA16	17	N.C.	70	N.C.	69	DATA15	GND
VCC_3.3V	DATA17	18	/WE	69	/CAS	68	DATA14	GND
VCC_3.3V	DATA18	19	/CAS	68	/RAS	67	DATA13	GND
VCC_3.3V	DATA19	20	/RAS	67	/CS	66	DATA12	GND
VCC_3.3V	DATA20	21	/CS	66	CKE	65	DATA11	GND
VCC_3.3V	DATA21	22	N.C.	65	A9	64	DATA10	GND
VCC_3.3V	DATA22	23	BA0	64	A8	63	DATA9	GND
VCC_3.3V	DATA23	24	BA1	63	A7	62	DATA8	GND
VCC_3.3V	DATA24	25	A10/AP	62	A6	61	DATA7	GND
VCC_3.3V	DATA25	26	A0	61	A5	60	DATA6	GND
VCC_3.3V	DATA26	27	A1	60	A4	59	DATA5	GND
VCC_3.3V	DATA27	28	A2	59	A3	58	DATA4	GND
VCC_3.3V	DATA28	29	DQM2	58	DQM3	57	DATA3	GND
VCC_3.3V	DATA29	30	VDD	57	VSS	56	DATA2	GND
VCC_3.3V	DATA30	31	N.C.	56	N.C.	55	DATA1	GND
VCC_3.3V	DATA31	32	DQ16	55	DQ31	54	DATA0	GND
VCC_3.3V	DATA32	33	VSSQ	54	VDDQ	53	DATA15	GND
VCC_3.3V	DATA33	34	DQ17	53	DQ30	52	DATA14	GND
VCC_3.3V	DATA34	35	DQ18	52	DQ29	51	DATA13	GND
VCC_3.3V	DATA35	36	VDD	51	VSSQ	50	DATA12	GND
VCC_3.3V	DATA36	37	DQ19	50	DQ28	49	DATA11	GND
VCC_3.3V	DATA37	38	DQ20	49	DQ27	48	DATA10	GND
VCC_3.3V	DATA38	39	VSSQ	48	VDDQ	47	DATA9	GND
VCC_3.3V	DATA39	40	DQ21	47	DQ26	46	DATA8	GND
VCC_3.3V	DATA40	41	DQ22	46	DQ25	45	DATA7	GND
VCC_3.3V	DATA41	42	VDDQ	45	VSSQ	44	DATA6	GND
VCC_3.3V	DATA42	43	DQ23	44	DQ24	43	DATA5	GND
VCC_3.3V	DATA43	44	VDD	43	VSS	42	DATA4	GND

Wilke Technology Project: TP3000				
Project No.:		PCB No.: PCB09.0400.V1.2		Sheet 3 of 11
File: P:\TP3000\PCB\PCB_Board_1V1.2\Source\TP3000_LCC.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype* pilot series* series	Changes documented in:
17. Jul 2009	V1.0	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
18. Sep 2009	V1.1	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
20. Jul 2010	V1.2	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
			• •	



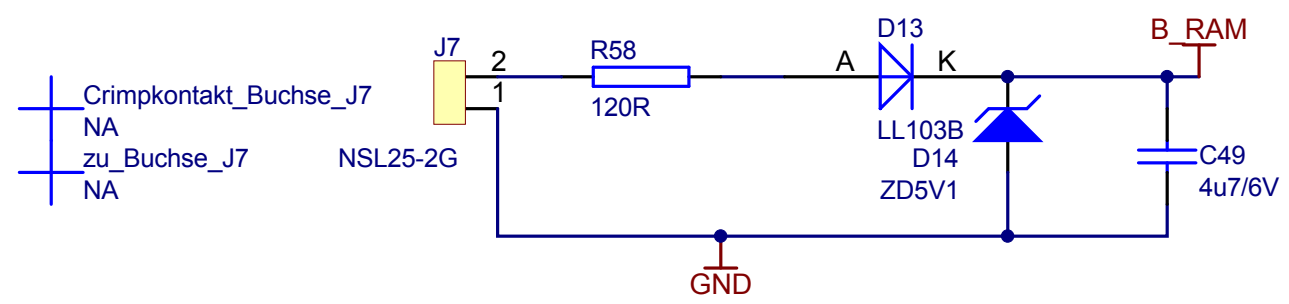
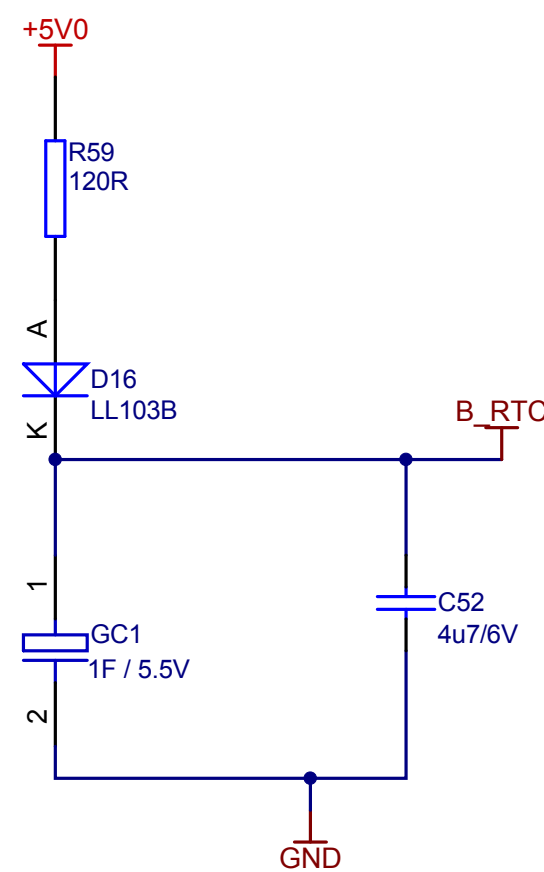
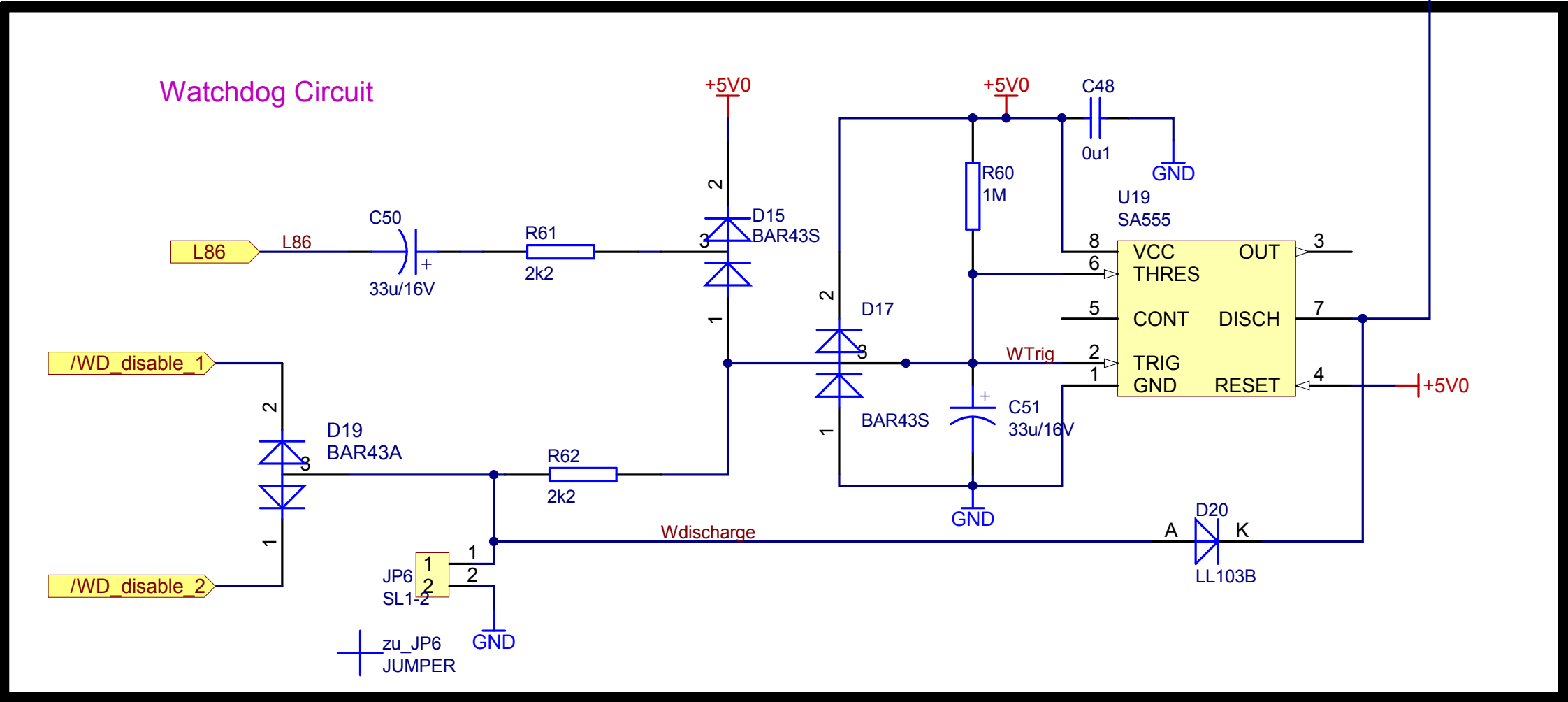
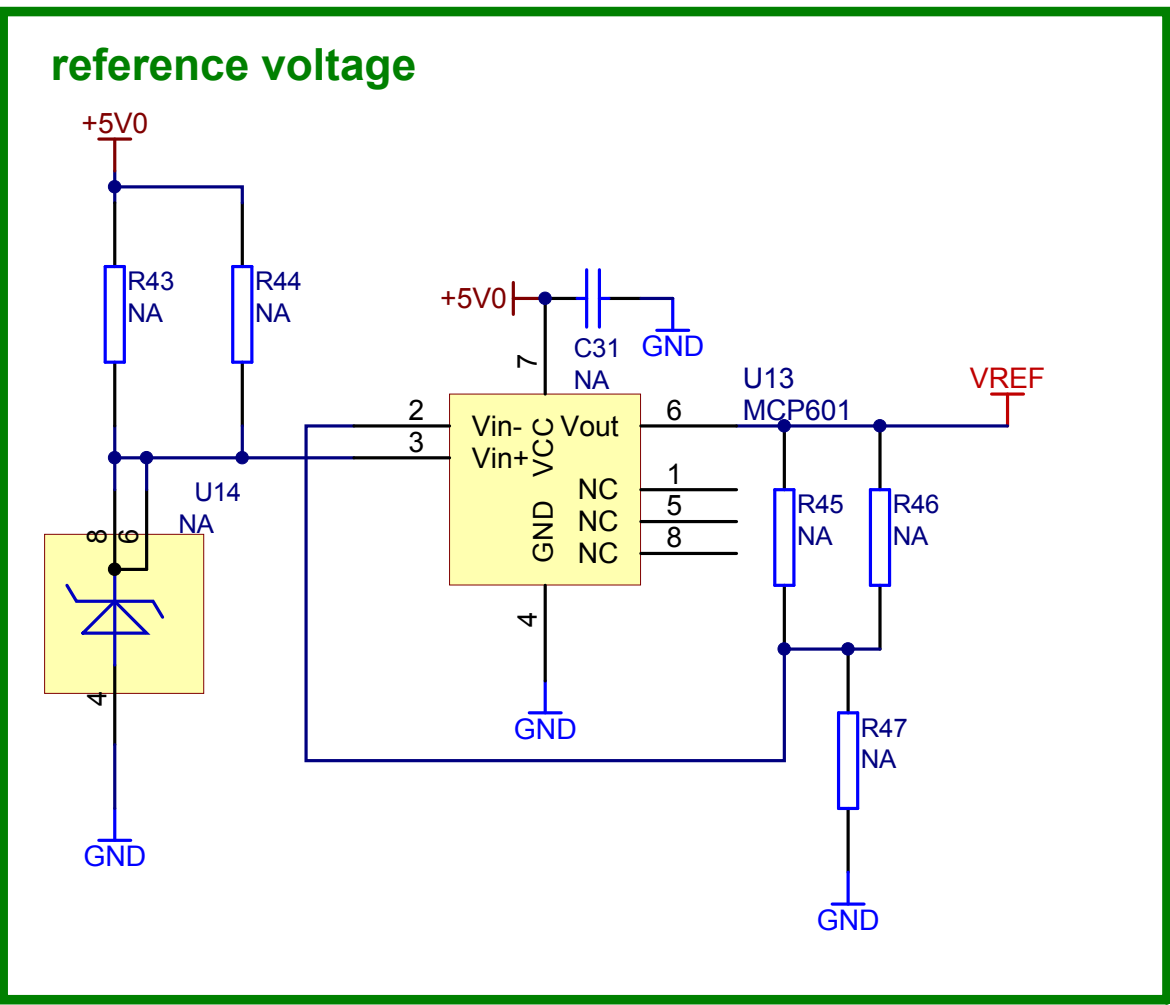
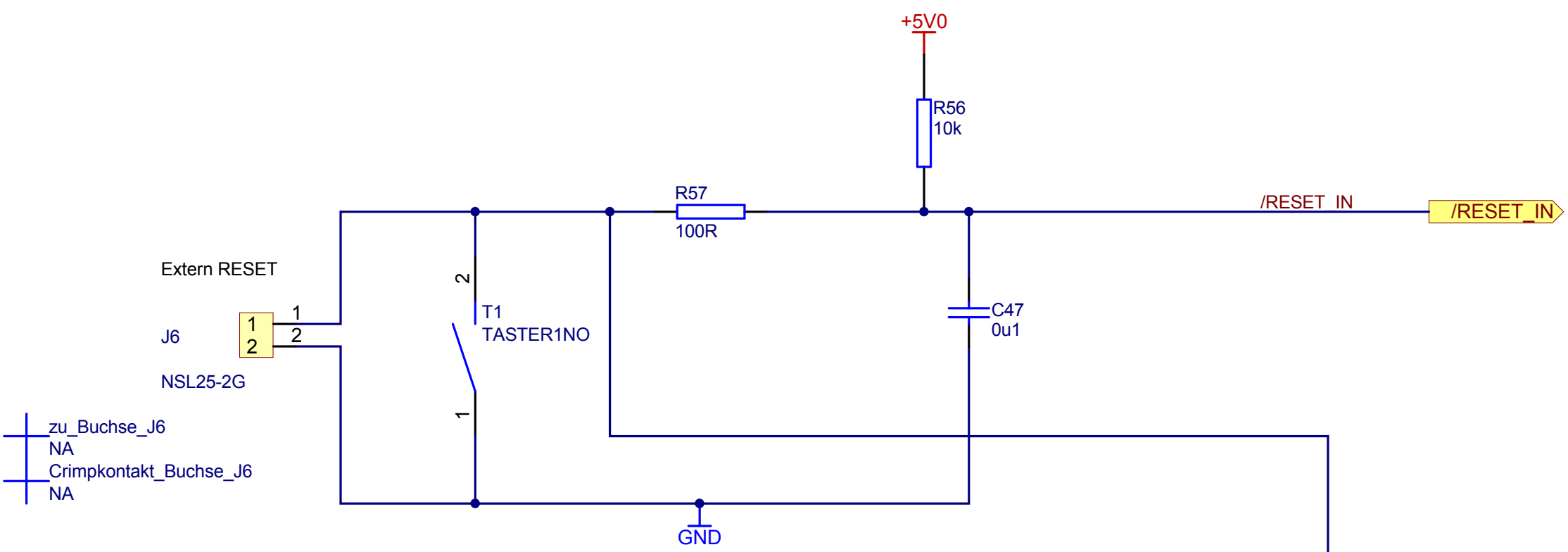
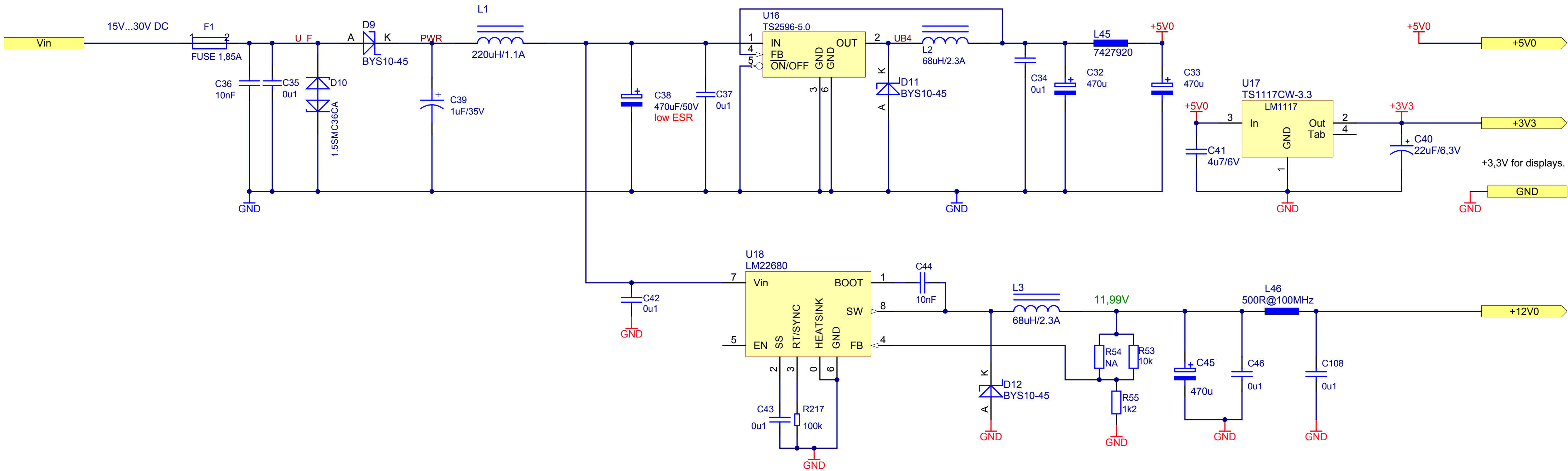
Wilke Technology Project: TP3000				
Project No.:		PCB No.: PCB09.0400.V1.2		Sheet 4 of 11
File: P:\TP3000\PCB\PCB_Board_1V1.2\Source\TP3000_comm.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype* pilot series* series	Changes documented in:
27. Juli 2009	V1.0	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
18. Sep 2009	V1.1	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
30. Jun 2010	V1.2	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
			• •	
			• •	



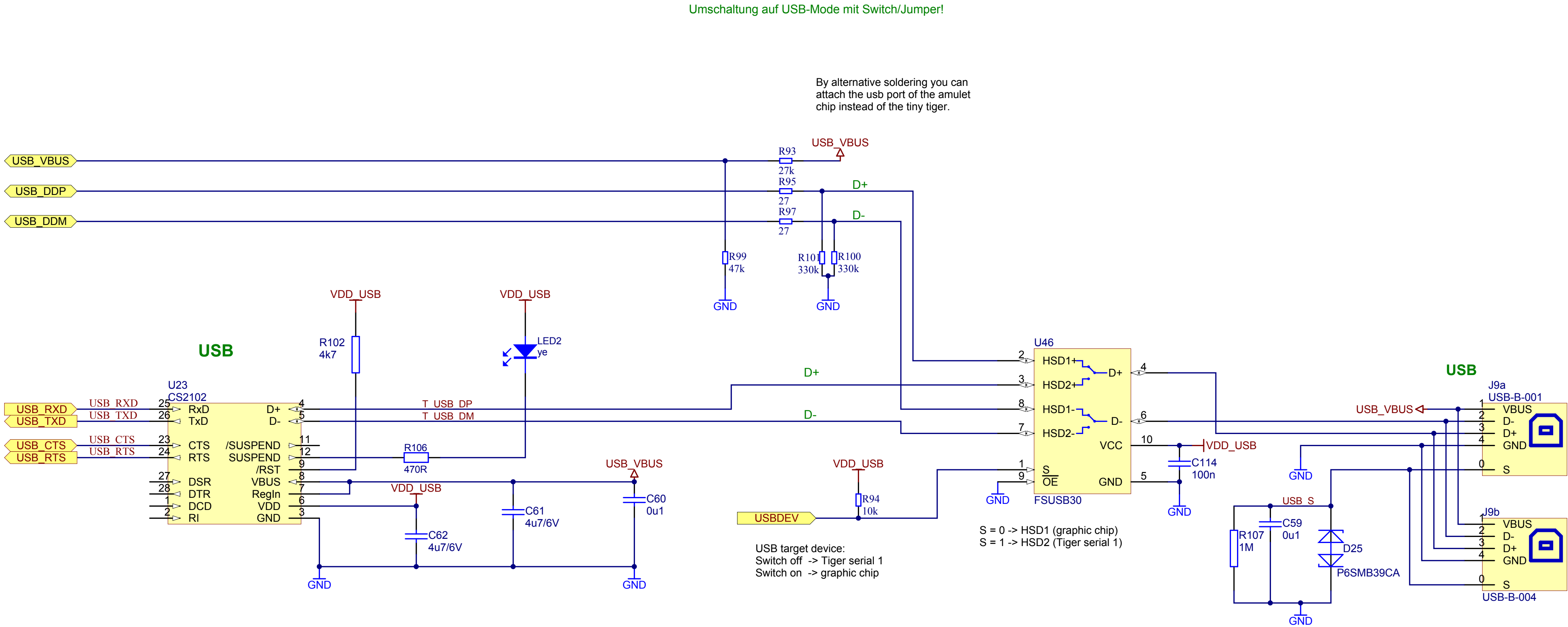


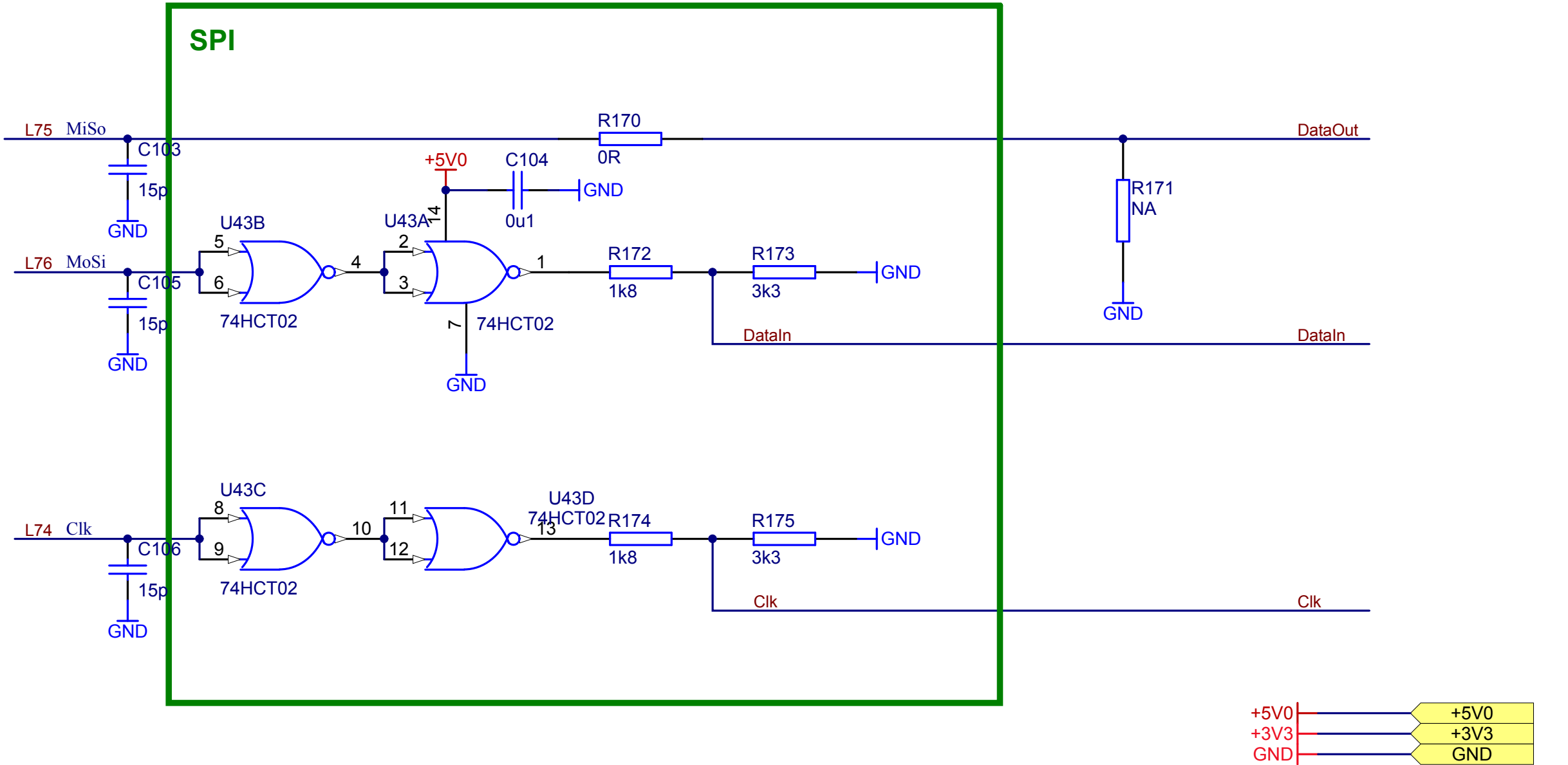
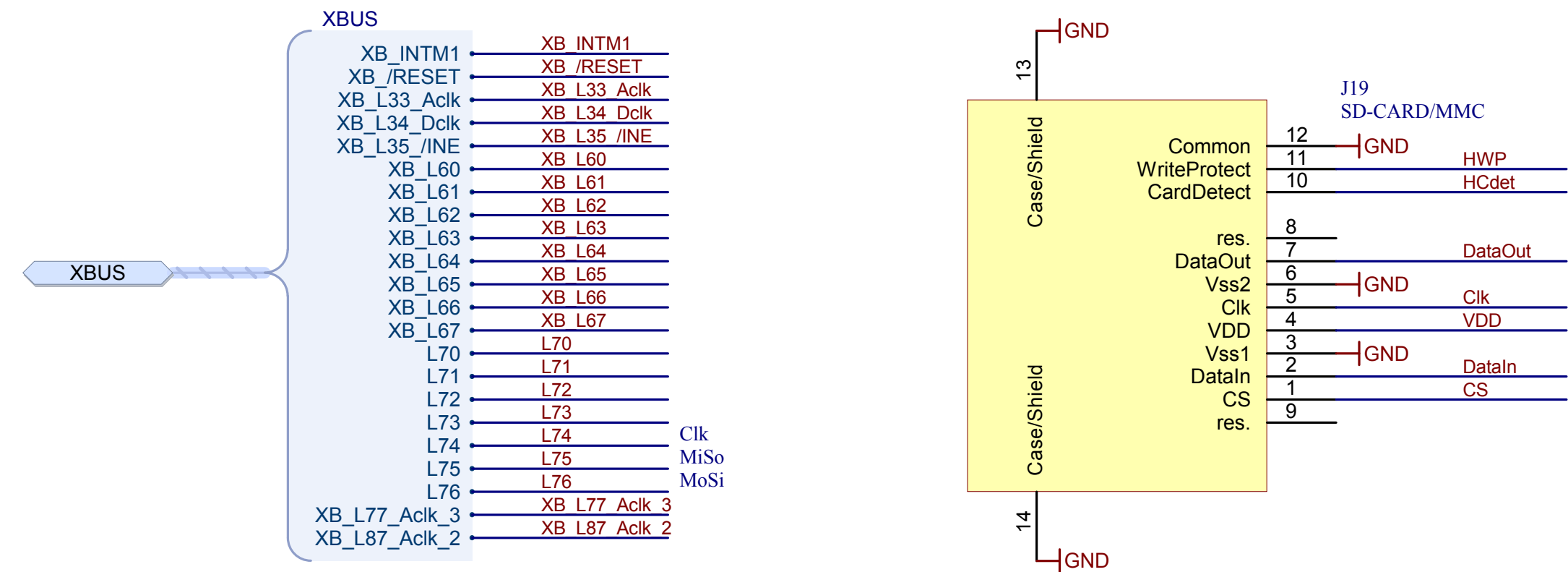
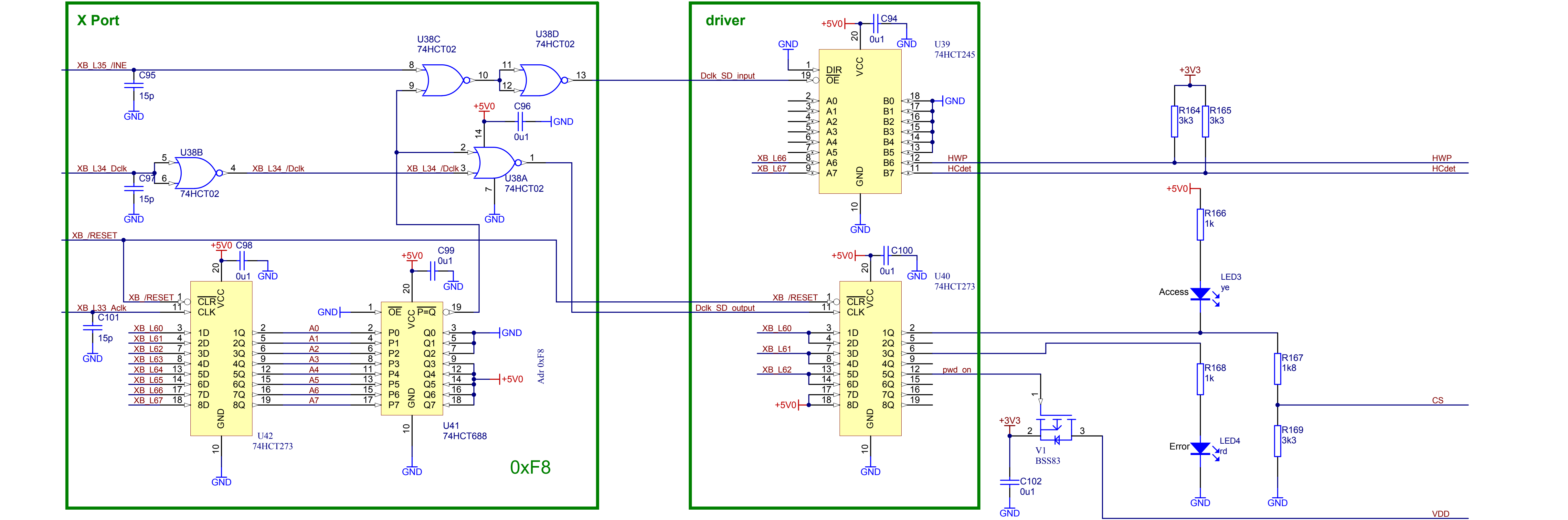
TP3000

power supply

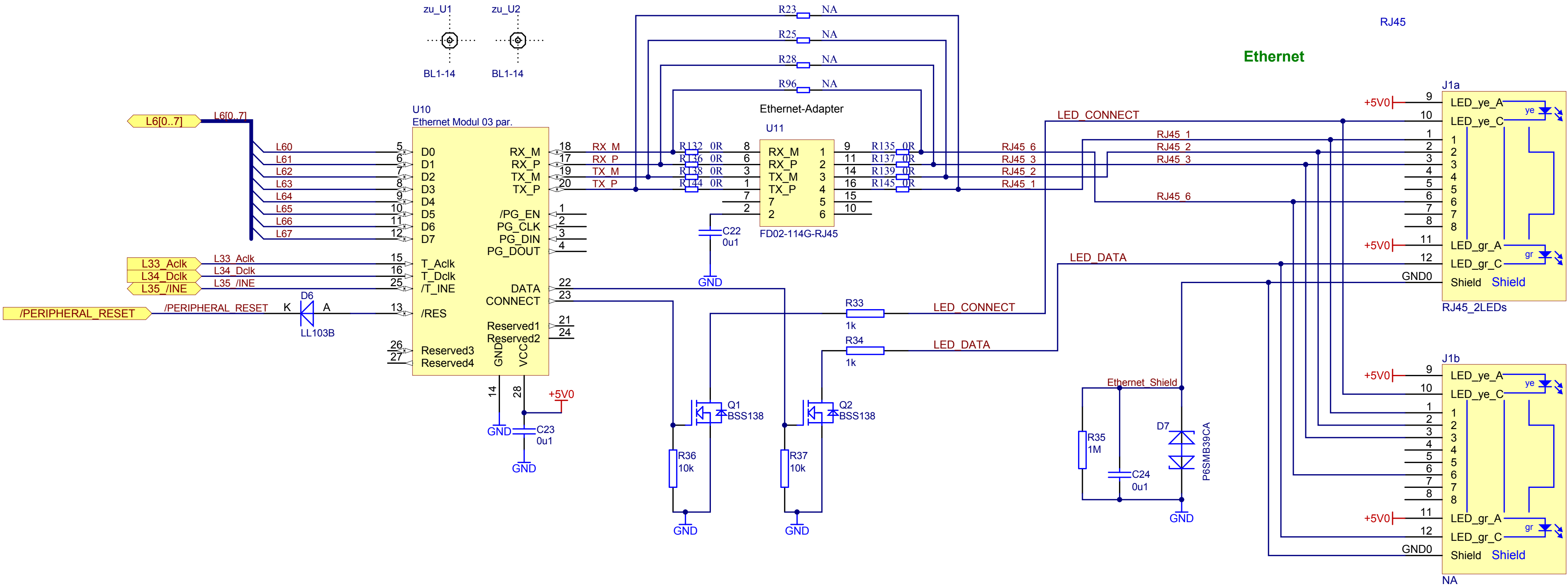


Wilke Technology Project: TP3000				
Project No.:		PCB No.: PCB09.0400.V1.2		Sheet 7 of 11
File: P:\TP3000\PCB\PCB_Board_1\V1.2\Source\TP3000_power.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype* pilot series* series	Changes documented in:
03. Aug 2009	V1.0	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
18. Sep 2009	V1.1	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
19. Jul 2010	V1.2	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
			• •	
			• •	

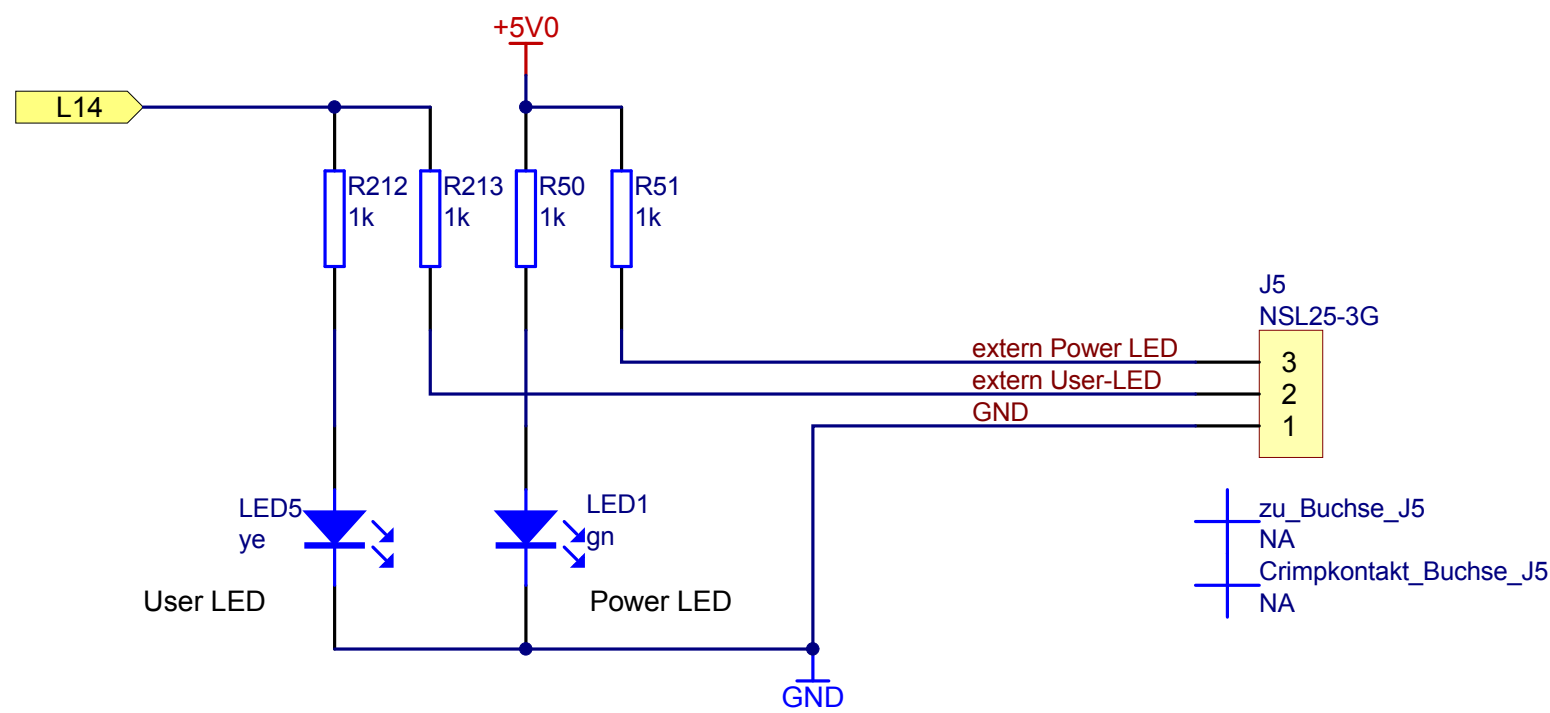
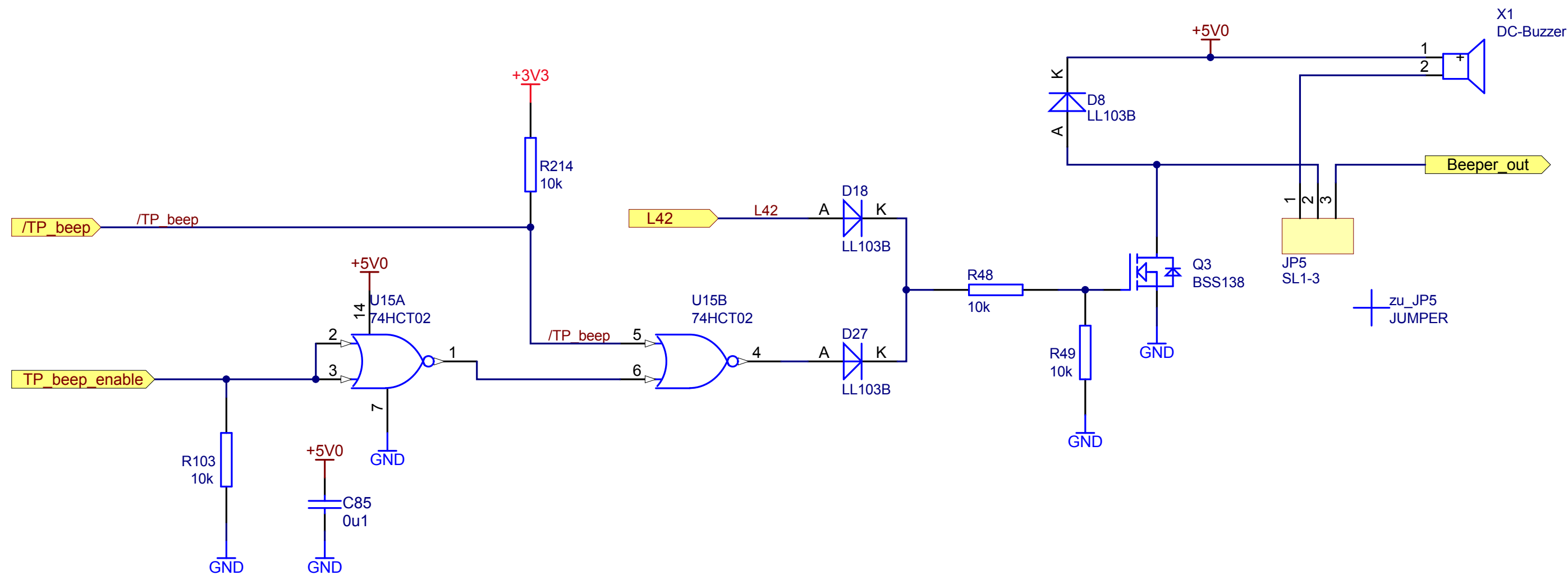
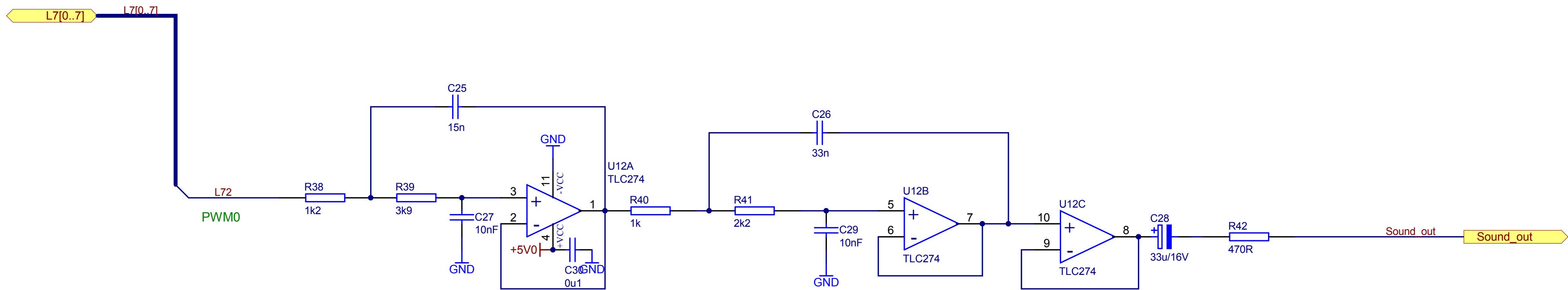




Wilke Technology Project:			TP3000	
Project No.:		PCB No.: PCB09.0400.V1.2		Sheet 9 of 11
File: P:\TP3000\PCB\PCB Board_1\1.2\Source\TP3000_SD-Card.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype: pilot series series	Changes documented in:
28.07.2009 18. Sep 2009	V1.0 V1.1	P. Grusdat P. Grusdat		P:\TP3000\PCB\History_TP3000.doc P:\TP3000\PCB\History_TP3000.doc



Wilke Technology Project: TP3000				
Project No.:		PCB No.: PCB09.0400.V1.2		Sheet 10 of 11
File: P:\TP3000\PCB\PCB_Board_1\1V1.2\Source\TP3000_ethernet.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype* pilot series* series	Changes documented in:
17. Jul 2009 20. Jul 2010	V1.0 V1.2	P. Grusdat P. Grusdat		P:\TP3000\PCB\History_TP3000.doc P:\TP3000\PCB\History_TP3000.doc



pcb1
PCB
Leiterplatte

Wilke Technology Project: TP3000				
Project No.:		PCB No.: PCB09.0400.V1.2		Sheet 11 of 11
File: P:\TP3000\PCB\PCB_Board_1\1.2\Source\TP3000_mech.SchDoc				
Date:	Revision:	Designer:	Release of PCB for Prototype* pilot series* series	Changes documented in:
18. Aug 2009	V1.0	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
18. Sep 2009	V1.1	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
08. Jul 2010	V1.2	P. Grusdat	• •	P:\TP3000\PCB\History_TP3000.doc
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